

Model Name: GA-B75M-D3H

Revision 1.1

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI SLOT1,2
17	ITE 8728 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV,-PROCHOT
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC887-VD2
23	REAR AUDIO JACK
24	REALTEK RTL8111F-VL
25	DISCRETE POWER
26	ATX,CLK GEN
27	RT8120_CPU_VTT

SHEET TITLE

28	VCORE ISL95836_1
29	VCORE ISL95836_2
30	RT8120_DDR POWER
31	LPT,TPM,M3 POWER
32	HDMI,DVI

Gigabyte Technology

Title			Cover Sheet
Size	Document Number	GA-B75M-D3H	Rev
Custom			1.1
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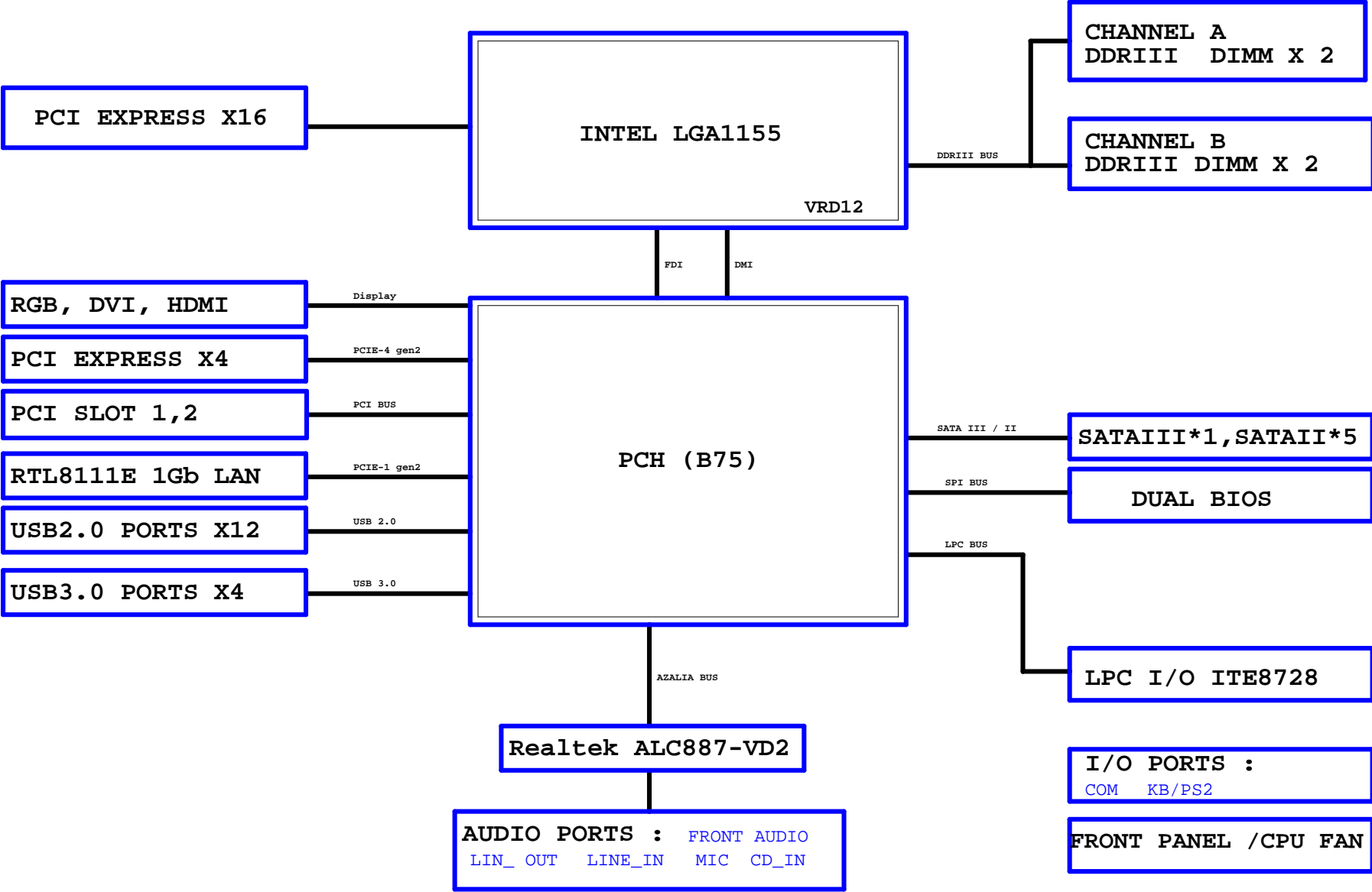
## Revision 1.1

## Component value change history

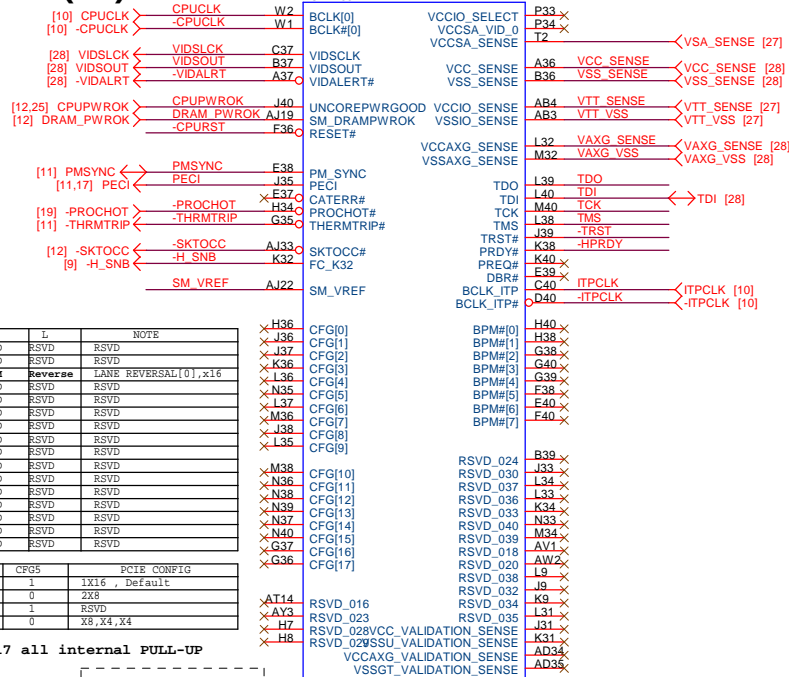
2012/06/01

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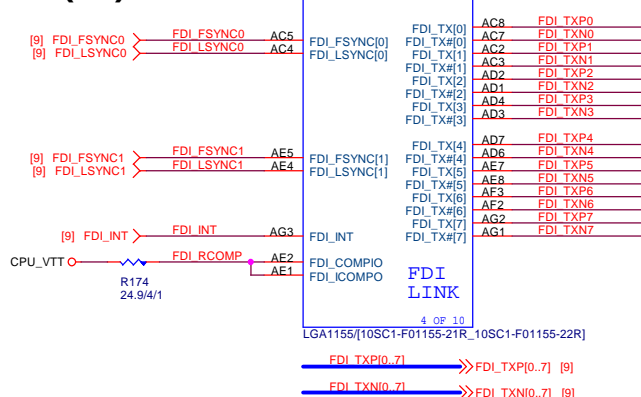
BLOCK DIAGRAM



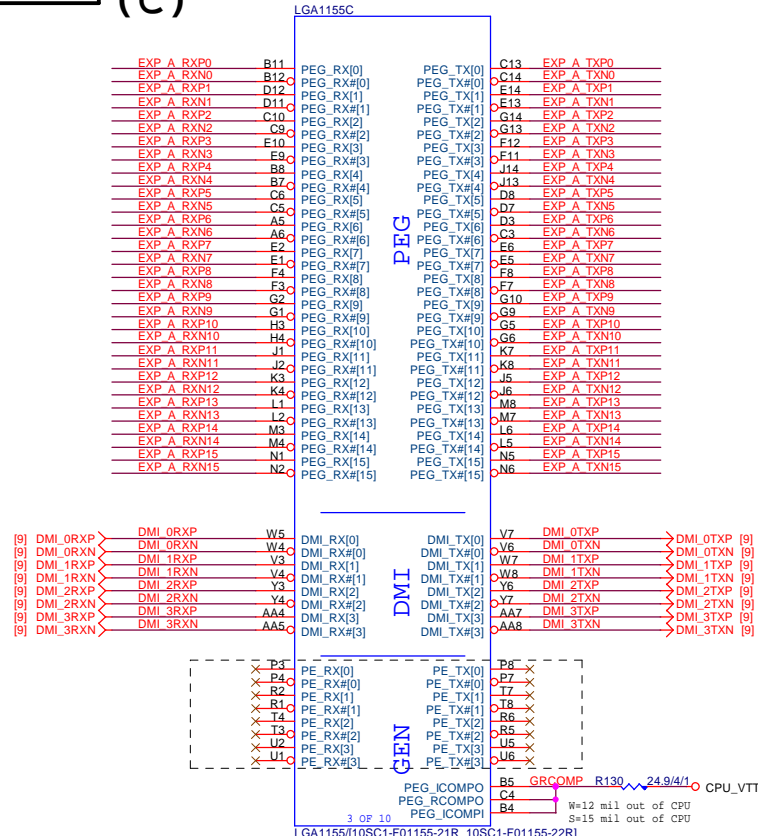
**LGA1155 (E)**



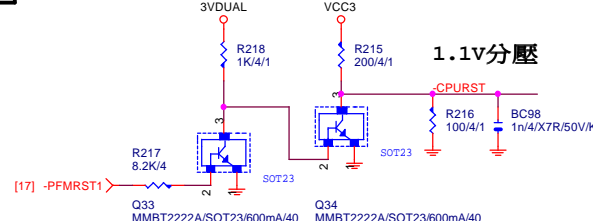
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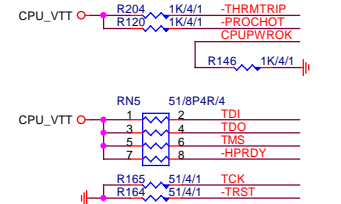
LGA1155 (C)



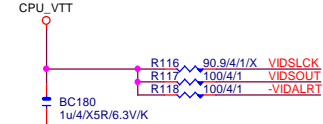
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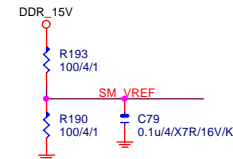
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98	98
99	99
100	100



## CPU SVID



## SM REF



LGA1155A

MAAA0	AV27	SA_MA[0]
MAAA1	AY24	SA_MA[1]
MAAA2	AW24	SA_MA[2]
MAAA3	AW23	SA_MA[3]
MAAA4	AV23	SA_MA[4]
MAAA5	AT23	SA_MA[5]
MAAA6	AT23	SA_MA[6]
MAAA7	AU22	SA_MA[7]
MAAA8	AV22	SA_MA[8]
MAAA9	AT22	SA_MA[9]
MAAA10	AV28	SA_MA[10]
MAAA11	AU21	SA_MA[11]
MAAA12	AT21	SA_MA[12]
MAAA13	AW32	SA_MA[13]
MAAA14	AU20	SA_MA[14]
MAAA15	AT20	SA_MA[15]

[7] -SWEA	← -SWEA	AW29	SA_WE#
[7] -SCASA	← -SCASA	AV30	SA_CAS#
[7] -SRASA	← -SRASA	AU28	SA_RAS#

[7] SBAA0	← SBAA0	AY29	SA_BS[0]
[7] SBAA1	← SBAA1	AW28	SA_BS[1]
[7] SBAA2	← SBAA2	AV20	SA_BS[2]

[7] -CSA0	← -CSA0	AU29	SA_CS#
[7] -CSA1	← -CSA1	AV32	SA_CS#
[7] -CSA2	← -CSA2	AW30	SA_CS#
[7] -CSA3	← -CSA3	AU33	SA_CS#

[7] CKEA0	← CKEA0	AV19	SA_CKE[0]
[7] CKEA1	← CKEA1	AT19	SA_CKE[1]
[7] CKEA2	← CKEA2	AU18	SA_CKE[2]
[7] CKEA3	← CKEA3	AV18	SA_CKE[3]

MODT_A0	AV31	SA_ODT[0]
MODT_A1	AU32	SA_ODT[1]
MODT_A2	AU30	SA_ODT[2]
MODT_A3	AW33	SA_ODT[3]

[7] DCLKA0	← DCLKA0	AY25	SA_CK[0]
[7] -DCLKA0	← -DCLKA0	AW25	SA_CK#
[7] DCLKA1	← DCLKA1	AU24	SA_CK[1]
[7] -DCLKA1	← -DCLKA1	AU25	SA_CK#
[7] DCLKA2	← DCLKA2	AW27	SA_CK[2]
[7] -DCLKA2	← -DCLKA2	AY27	SA_CK#
[7] DCLKA3	← DCLKA3	AV26	SA_CK[3]
[7] -DCLKA3	← -DCLKA3	AW26	SA_CK#

[7,8] -DDR3\_RST ← -DDR3\_RST AW18 SM\_DRAMRST#

SA_DQS[0]	AK3	DQSA0
SA_DQS#0	AK2	-DQSA0

SA_DQ[0]	AJ3	MDA0
SA_DQ[1]	AJ4	MDA1
SA_DQ[2]	AL3	MDA2
SA_DQ[3]	AL4	MDA3
SA_DQ[4]	AJ2	MDA4
SA_DQ[5]	AJ1	MDA5
SA_DQ[6]	AL2	MDA6
SA_DQ[7]	AL1	MDA7

SA_DQS[1]	AP3	DQSA1
SA_DQS#1	AP2	-DQSA1

SA_DQ[8]	AN1	MDA8
SA_DQ[9]	AN4	MDA9
SA_DQ[10]	AR3	MDA10
SA_DQ[11]	AR4	MDA11
SA_DQ[12]	AN2	MDA12
SA_DQ[13]	AN3	MDA13
SA_DQ[14]	AR2	MDA14
SA_DQ[15]	AR1	MDA15

SA_DQS[2]	AW4	DQSA2
SA_DQS#2	AV4	-DQSA2

SA_DQ[16]	AV2	MDA16
SA_DQ[17]	AW3	MDA17
SA_DQ[18]	AV5	MDA18
SA_DQ[19]	AW5	MDA19
SA_DQ[20]	AJ3	MDA21
SA_DQ[21]	AU5	MDA22
SA_DQ[22]	AY5	MDA23
SA_DQ[23]		

SA_DQS[3]	AV8	DQSA3
SA_DQS#3	AW8	-DQSA3

SA_DQ[24]	AY7	MDA24
SA_DQ[25]	AU7	MDA25
SA_DQ[26]	AV9	MDA26
SA_DQ[27]	AU9	MDA27
SA_DQ[28]	AV7	MDA28
SA_DQ[29]	AW7	MDA29
SA_DQ[30]	AW9	MDA30
SA_DQ[31]	AY9	MDA31

SA_DQS[4]	AV37	DQSA4
SA_DQS#4	AV36	-DQSA4

SA_DQ[32]	AU35	MDA32
SA_DQ[33]	AW37	MDA33
SA_DQ[34]	AU39	MDA34
SA_DQ[35]	AU36	MDA35
SA_DQ[36]	AW35	MDA36
SA_DQ[37]	AY36	MDA37
SA_DQ[38]	AU38	MDA38
SA_DQ[39]	AU37	MDA39

SA_DQS[5]	AP38	DQSA5
SA_DQS#5	AP39	-DQSA5

SA_DQ[40]	AR40	MDA40
SA_DQ[41]	AR37	MDA41
SA_DQ[42]	AN38	MDA42
SA_DQ[43]	AN37	MDA43
SA_DQ[44]	AR38	MDA44
SA_DQ[45]	AN39	MDA45
SA_DQ[46]	AN40	MDA46
SA_DQ[47]		

SA_DQS[6]	AK38	DQSA6
SA_DQS#6	AK39	-DQSA6

SA_DQ[48]	AL40	MDA48
SA_DQ[49]	AL37	MDA49
SA_DQ[50]	AJ38	MDA50
SA_DQ[51]	AJ37	MDA51
SA_DQ[52]	AL39	MDA52
SA_DQ[53]	AL38	MDA53
SA_DQ[54]	AJ39	MDA54
SA_DQ[55]	AJ40	MDA55

SA_DQS[7]	AF38	DQSA7
SA_DQS#7	AF39	-DQSA7

SA_DQ[56]	AG40	MDA56
SA_DQ[57]	AG37	MDA57
SA_DQ[58]	AE38	MDA58
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SA_DQ[61]	AG38	MDA61
SA_DQ[62]	AE39	MDA62
SA_DQ[63]	AE40	MDA63

DDR\_0

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LGA1155[10SC1-F01155-21R\_10SC1-F01155-22R]

LGA1155B

MAAB0	AK24	SB_MA[0]
MAAB1	AM20	SB_MA[1]
MAAB2	AM19	SB_MA[2]
MAAB3	AK18	SB_MA[3]
MAAB4	AP18	SB_MA[4]
MAAB5	AP18	SB_MA[5]
MAAB6	AM18	SB_MA[6]
MAAB7	AL18	SB_MA[7]
MAAB8	AN18	SB_MA[8]
MAAB9	AY17	SB_MA[9]
MAAB10	AN23	SB_MA[10]
MAAB11	AU17	SB_MA[11]
MAAB12	AT18	SB_MA[12]
MAAB13	AR26	SB_MA[13]
MAAB14	AY16	SB_MA[14]
MAAB15	AV16	SB_MA[15]

[8] -SWEB	← -SWEB	AR25	SB_WE#
[8] -SCASB	← -SCASB	AK25	SB_CAS#
[8] -SRASB	← -SRASB	AP24	SB_RAS#

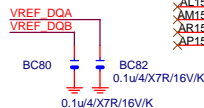
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[8] SBAB1	← SBAB1	AM24	SB_BS[1]
[8] SBAB2	← SBAB2	AW17	SB_BS[2]

[8] -CSB0	← -CSB0	AN25	SB_CS#
[8] -CSB1	← -CSB1	AN26	SB_CS#
[8] -CSB2	← -CSB2	AL25	SB_CS#
[8] -CSB3	← -CSB3	AT26	SB_CS#

[8] CKEB0	← CKEB0	AU16	SB_CKE[0]
[8] CKEB1	← CKEB1	AY15	SB_CKE[1]
[8] CKEB2	← CKEB2	AW15	SB_CKE[2]
[8] CKEB3	← CKEB3	AV15	SB_CKE[3]

MODT_B0	AL26	SB_ODT[0]
MODT_B1	AP26	SB_ODT[1]
MODT_B2	AM26	SB_ODT[2]
MODT_B3	AK26	SB_ODT[3]

[8] DCLKB0	← DCLKB0	AL21	SB_CK[0]
[8] -DCLKB0	← -DCLKB0	AL22	SB_CK#
[8] DCLKB1	← DCLKB1	AL20	SB_CK[1]
[8] -DCLKB1	← -DCLKB1	AK20	SB_CK#
[8] DCLKB2	← DCLKB2	AL23	SB_CK[2]
[8] -DCLKB2	← -DCLKB2	AM22	SB_CK#
[8] DCLKB3	← DCLKB3	AP21	SB_CK[3]
[8] -DCLKB3	← -DCLKB3	AN21	SB_CK#

VREF\_D0B AH1  
VREF\_D0A AH4

FC\_AH1

FC\_AH4

SA_DQS[8]	AN16	DQSB8
SA_DQS#8	AN15	-DQSB8

SA_DQS[9]	AM16	DQSB9
SA_DQS#9	AM15	-DQSB9

SA_DQS[10]	AR16	DQSB10
SA_DQS#10	AR15	-DQSB10

SA_DQS[11]	AM15	DQSB11
SA_DQS#11	AM14	-DQSB11

SA_DQS[12]	AR15	DQSB12
SA_DQS#12	AR14	-DQSB12

SA_DQS[13]	AM15	DQSB13
SA_DQS#13	AM14	-DQSB13

SA_DQS[14]	AR15	DQSB14
SA_DQS#14	AR14	-DQSB14

SA_DQS[15]	AM15	DQSB15
SA_DQS#15	AM14	-DQSB15

SA_DQS[16]	AM15	DQSB16
SA_DQS#16	AM14	-DQSB16

SA_DQS[17]	AG35	DQSB17
SA_DQS#17	AG34	-DQSB17

SA_DQS[18]	AH35	DQSB18
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SA_DQS[19]	AE34	DQSB19
SA_DQS#19	AE33	-DQSB19

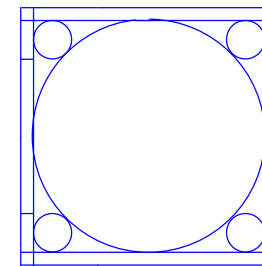
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SA_DQS[21]	AF33	DQSB21
SA_DQS#21	AF32	-DQSB21

SA_DQS[22]	AF35	DQSB22
SA_DQS#22	AF34	-DQSB22

DDR\_1

LGA1155[10SC1-F01155-21R\_10SC1-F01155-22R]

CR  
CPU RETENTION/X

LGA1155\_P

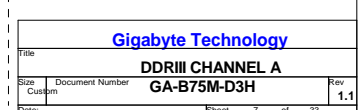
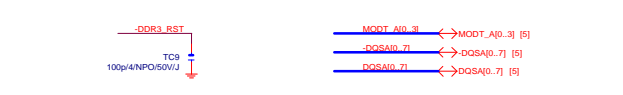


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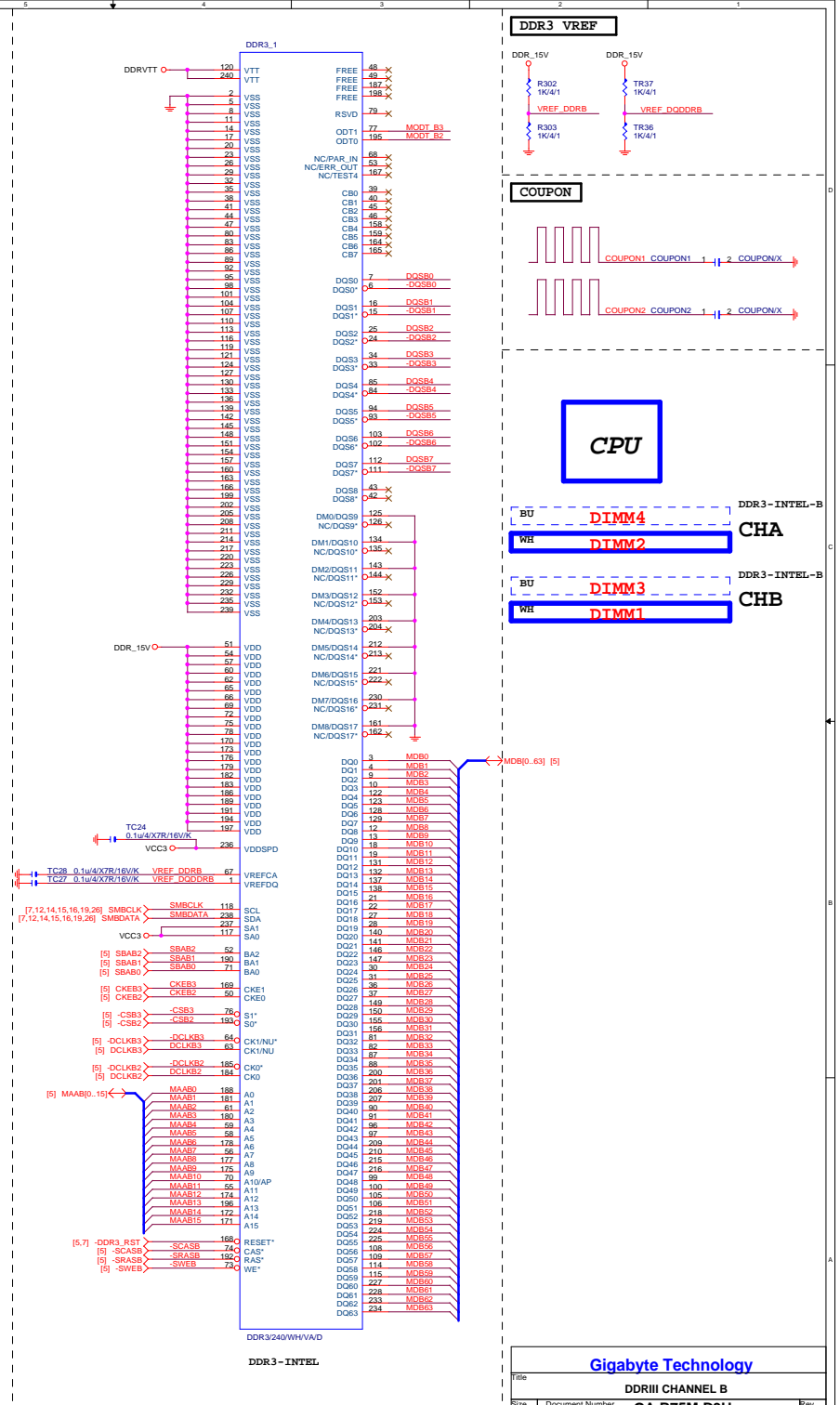
Gigabyte Technology

File			CPU LGA1156-B	
Size			Document Number	
Custom			GA-B75M-D3H	
Date:	Friday, June 08, 2012	Sheet	5	of 33
			1	Rev 1.1



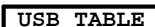






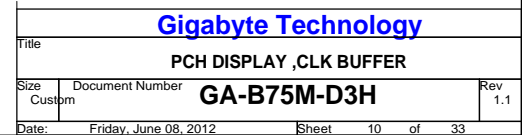


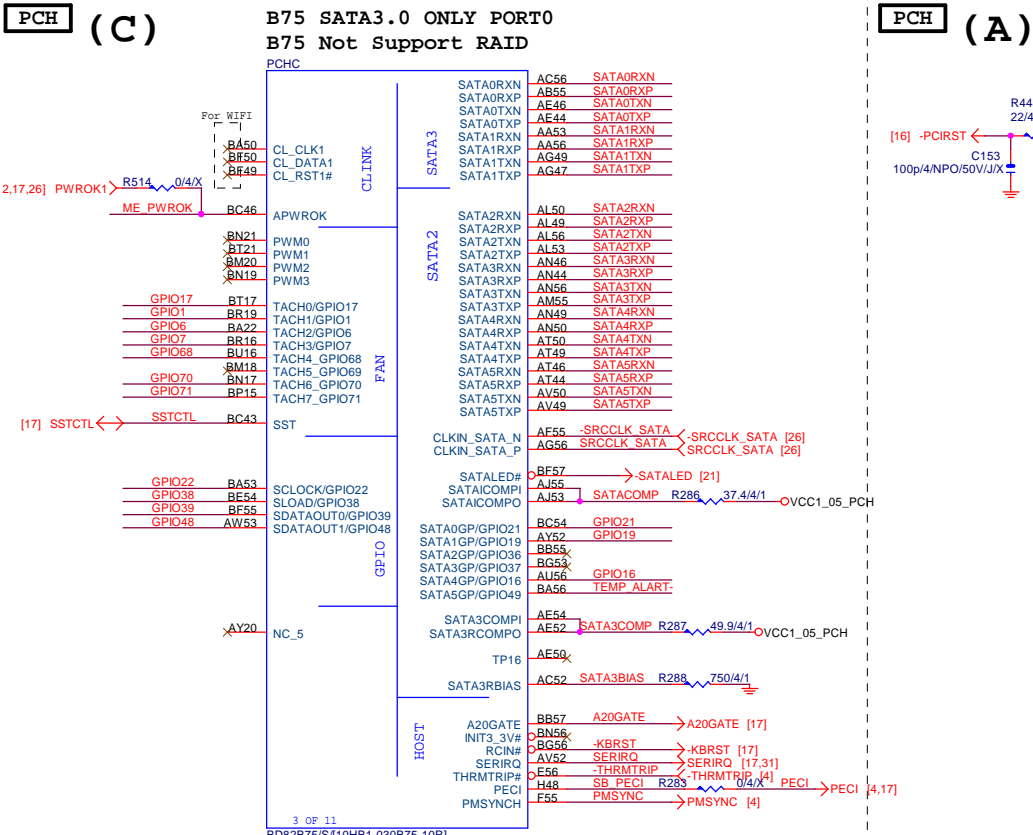
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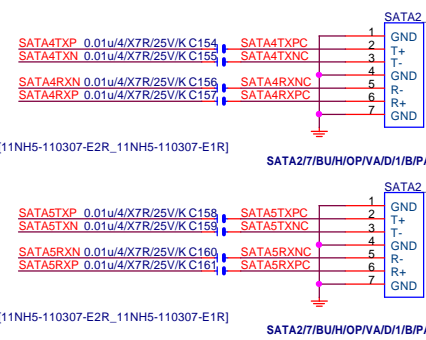
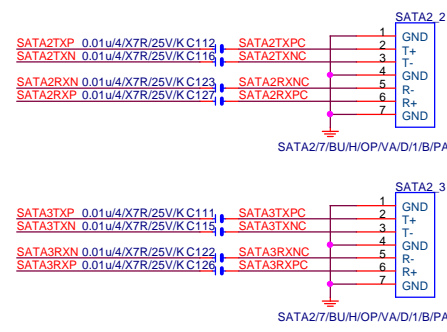
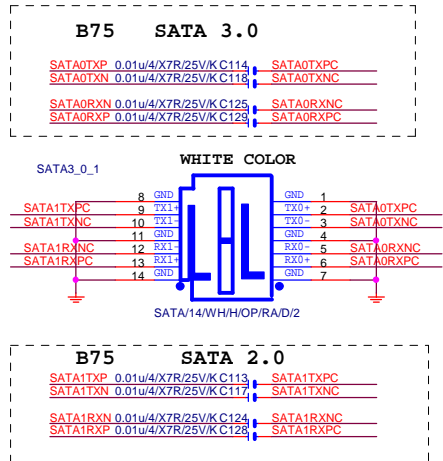
Date: Friday, June 08, 2012 Sheet 9 of 33

(F)

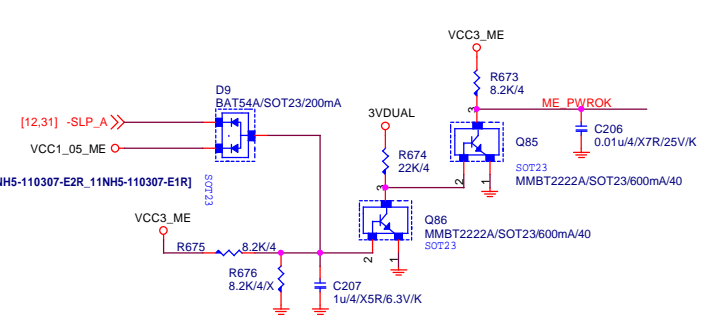




**SATA CONNECTOR H1X7-SATA2-HS-MASK**



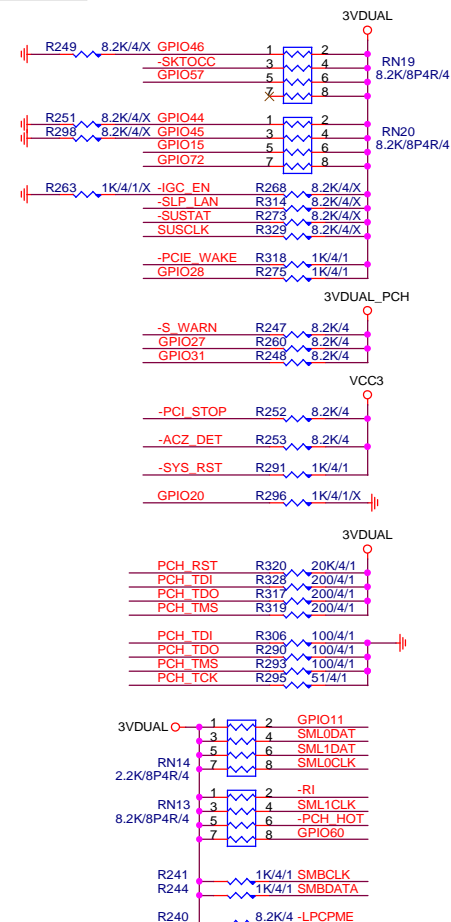
**ME PWROK**



**PCH (D)**

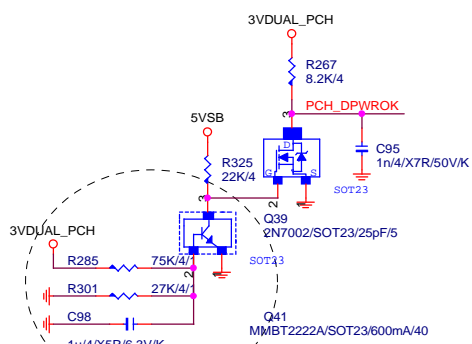


PCH PU/PD

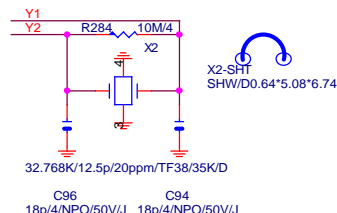


## PCH DPWROK

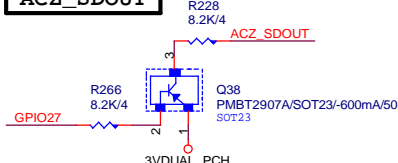
At least 10ms delay after  
3VDUAL\_PCH stable



32.768KHZ



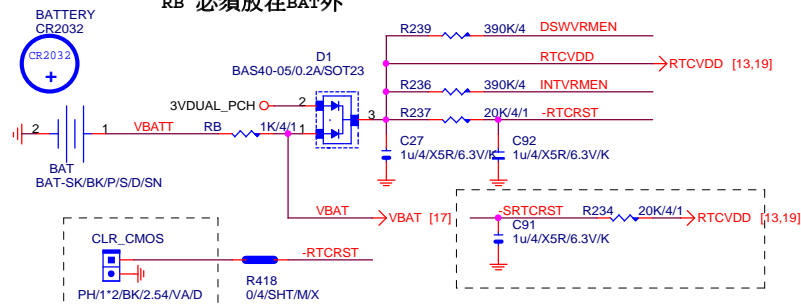
## ACZ\_SDOUT



## CLR CMOS

BATTERY-DUAL-4

RB 必須放在BAT外



## Gigabyte Technology

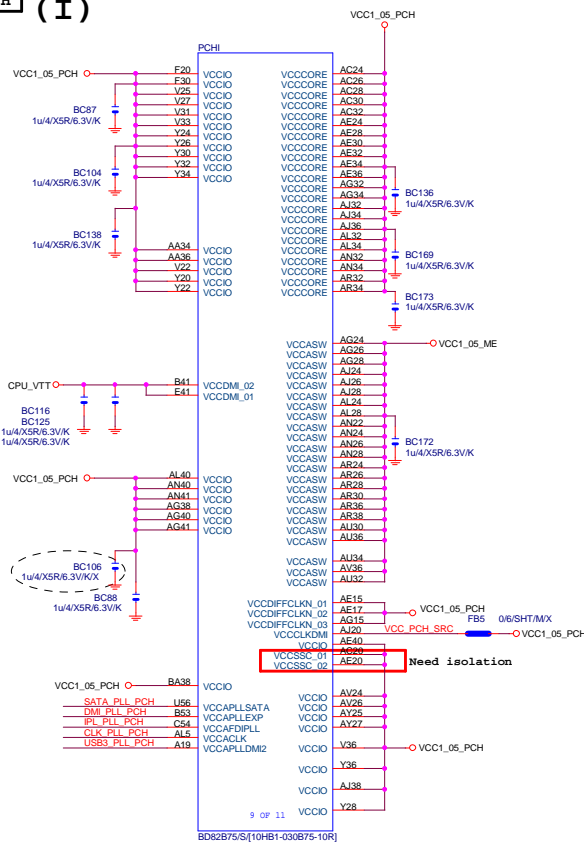
PCH GPIO . CTRL . AUDIO

GA-B75M-D3H

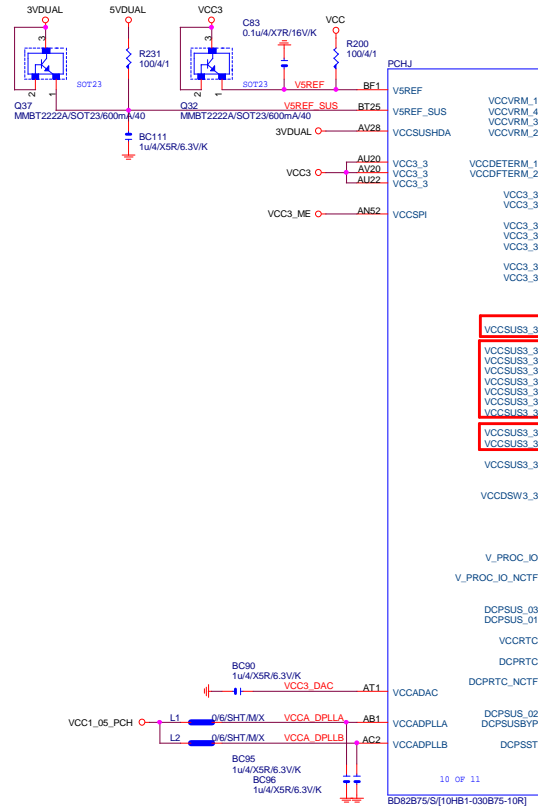
1.1

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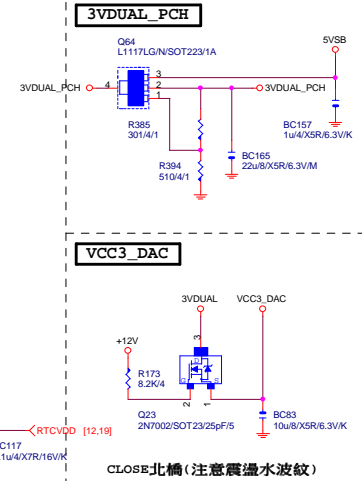
**PCH (I)**



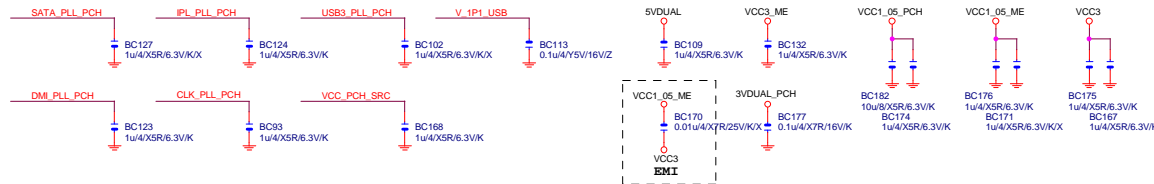
PCH



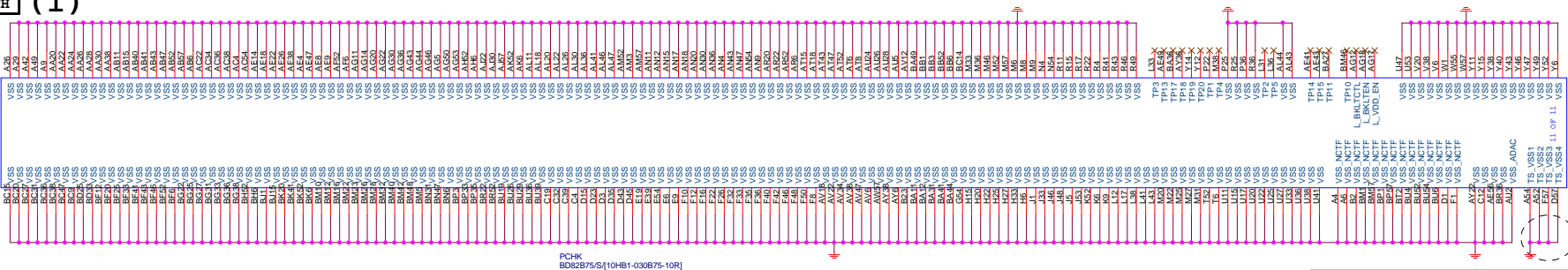
SHT PWR



## CAP

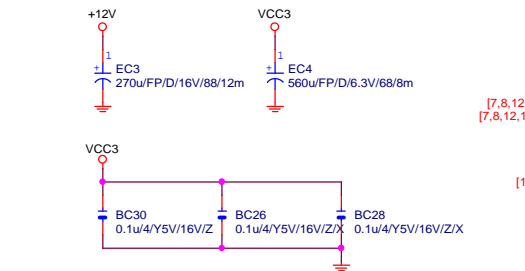


**PCH (I)**

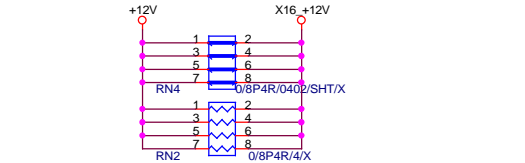




# PCIEX16 CAP



# PCIEX16 PROTECT SHT

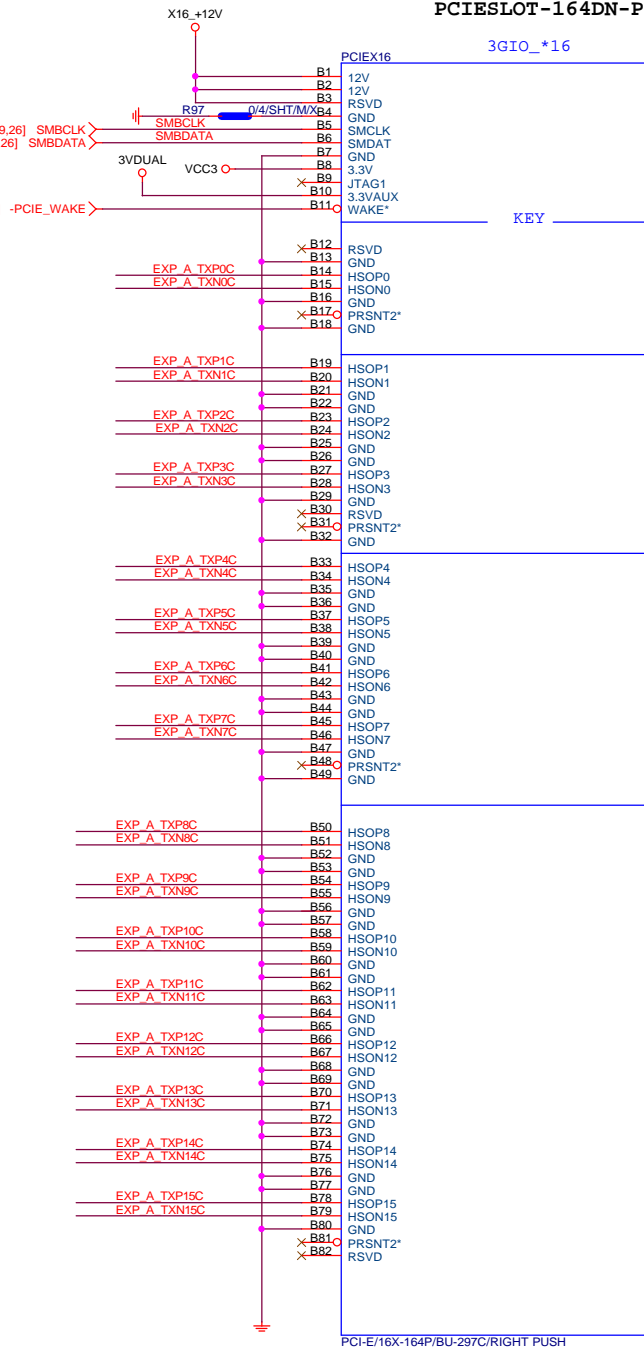


# PCIEX16 AC CAP

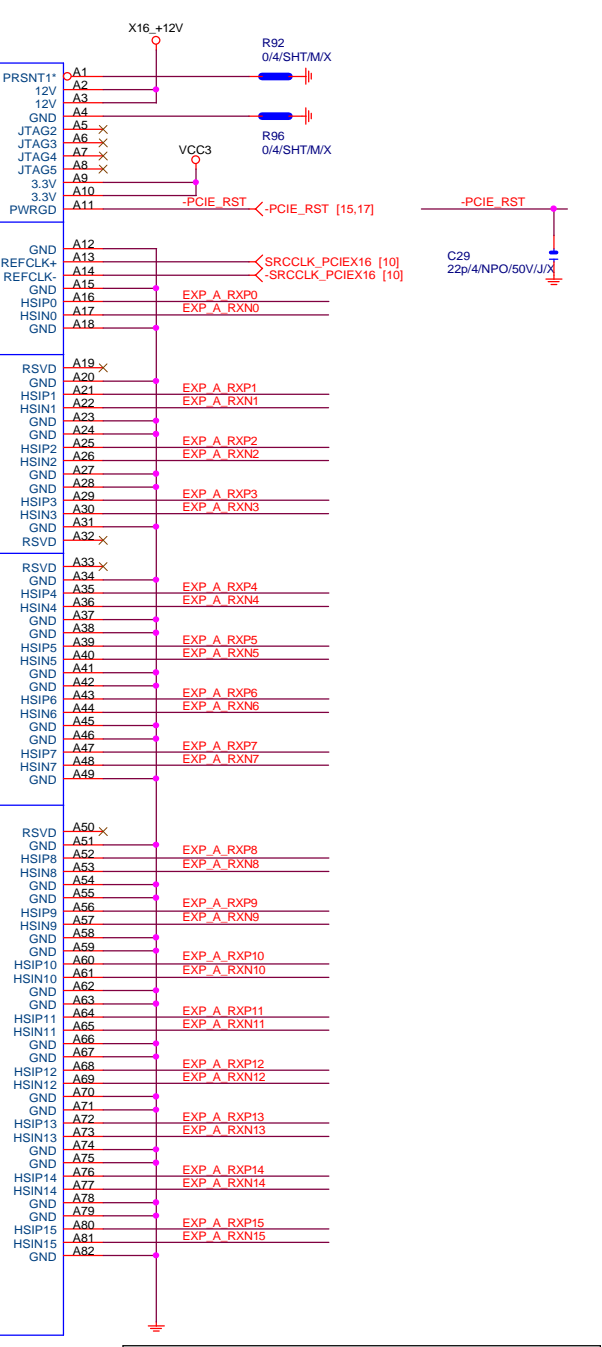
EXP A TXP0	C32	0.22u/4/X5R/6.3V/K	EXP A TXP0C
EXP A TXN0	C30	0.22u/4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C35	0.22u/4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C37	0.22u/4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C39	0.22u/4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C41	0.22u/4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C43	0.22u/4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C45	0.22u/4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C46	0.22u/4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C49	0.22u/4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C50	0.22u/4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C51	0.22u/4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C52	0.22u/4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C54	0.22u/4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C57	0.22u/4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C58	0.22u/4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C60	0.22u/4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C61	0.22u/4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C62	0.22u/4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C63	0.22u/4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C64	0.22u/4/X5R/6.3V/K	EXP A TXP10C
EXP A TXN10	C65	0.22u/4/X5R/6.3V/K	EXP A TXN10C
EXP A TXP11	C66	0.22u/4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C67	0.22u/4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C68	0.22u/4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C70	0.22u/4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C72	0.22u/4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C73	0.22u/4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C74	0.22u/4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C75	0.22u/4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C77	0.22u/4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C78	0.22u/4/X5R/6.3V/K	EXP A TXN15C

EXP A RXP0[0..15] >>> EXP\_A\_RXP[0..15] [4]  
EXP A RXN0[0..15] >>> EXP\_A\_RXN[0..15] [4]  
EXP A TXP0[0..15] >>> EXP\_A\_TXP[0..15] [4]  
EXP A TXN0[0..15] >>> EXP\_A\_TXN[0..15] [4]

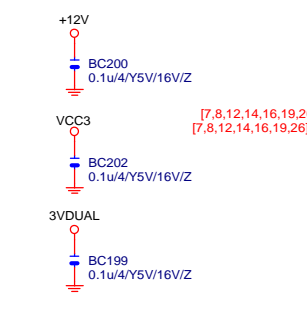
# PCIEX16 SLOT



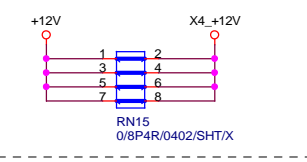
# PCIESLOT-164DN-P



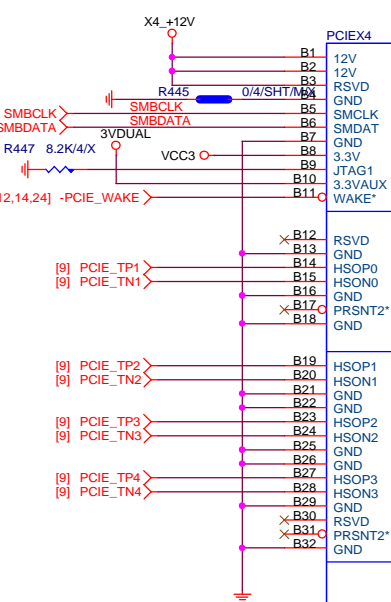
PCIEX4 CAP



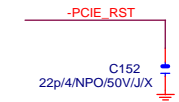
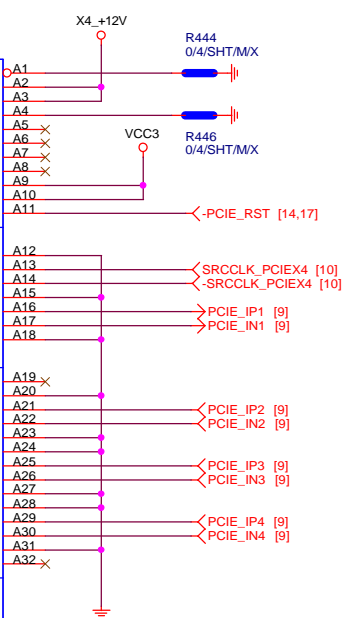
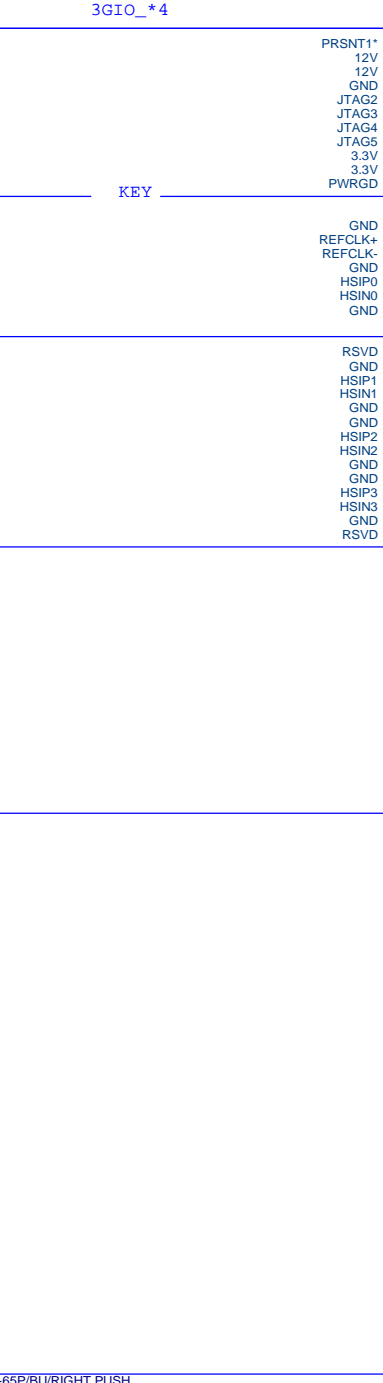
PCIEX16 PROTECT SHT



PCIEX4 SLOT

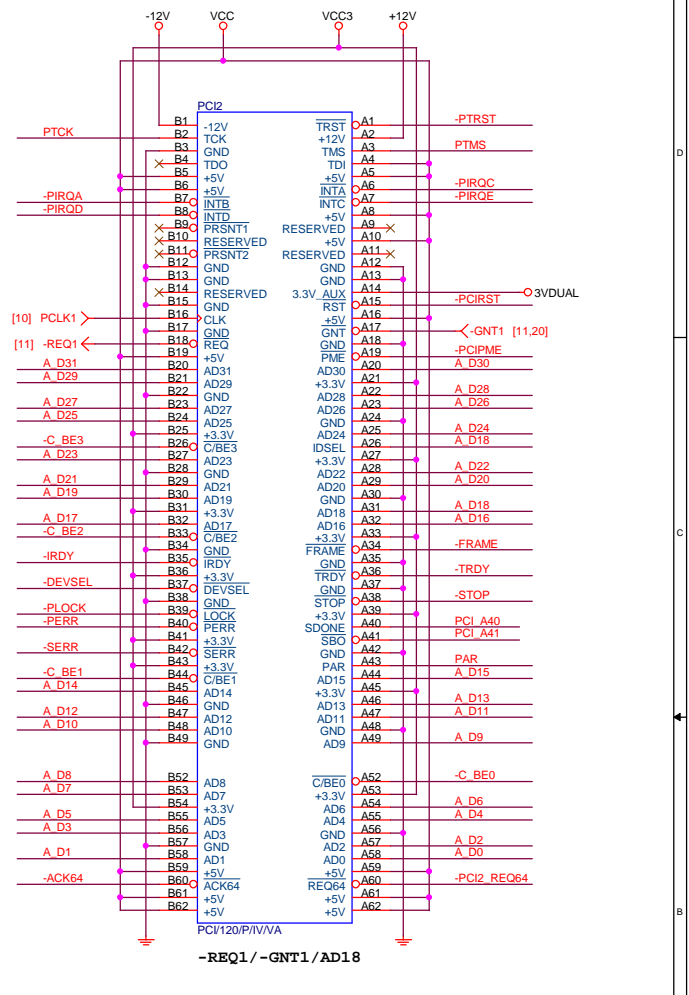
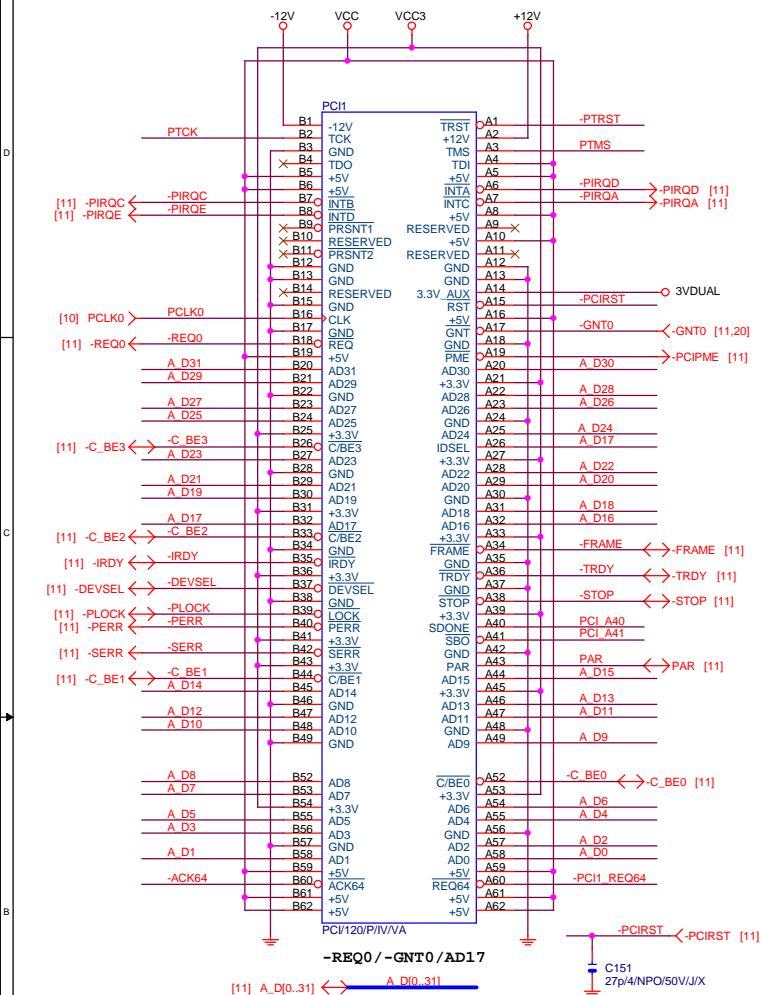


PCIESLOT-64D-98D-P

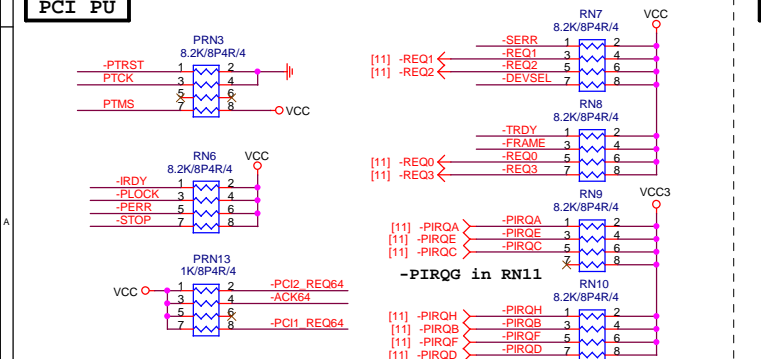




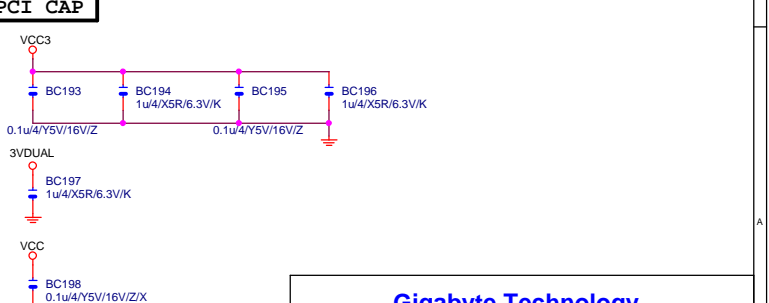
PCI SLOT



## PCI PU



## PCI CAP



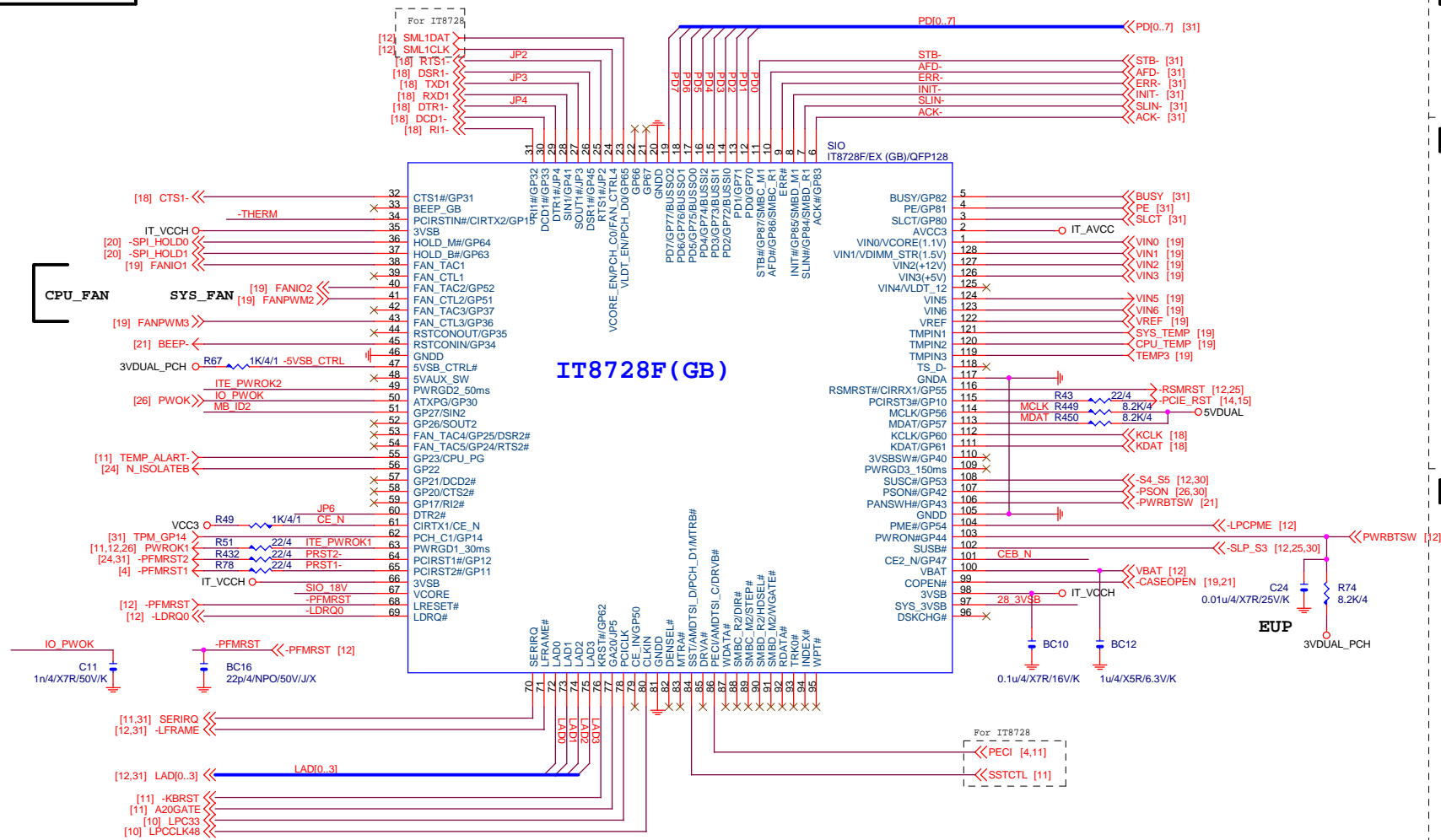
## Gigabyte Technology

## PCI SLOT 1&2

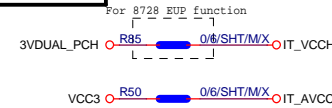
GA-B75M-D3H

### 1.1

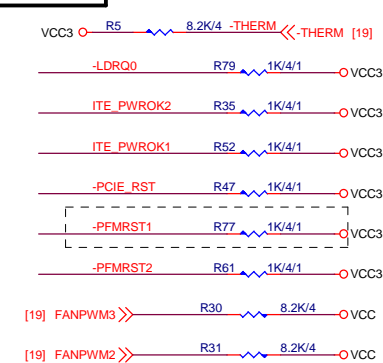
## SIO IT8728F



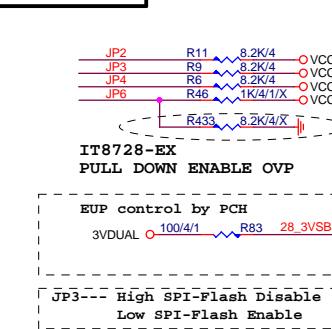
## PWR SHT



## SIO PU



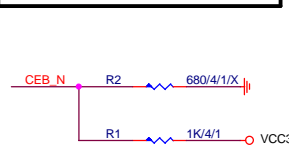
## SIO STRAP



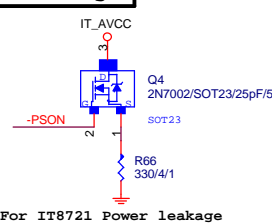
## IT8728F NOTE

	IT8728
PIN121	VCORE_EN/PCH_C0
PIN120	VLDT_EN/PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRV#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2 (VCC5)
PIN96	VIN1 (VCC12)
PIN97	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0/VCORE(1.1V)/NC

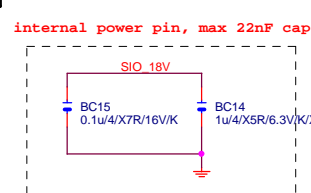
## DUAL BIOS OPT STRAP



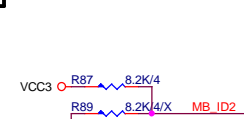
## Power leakage



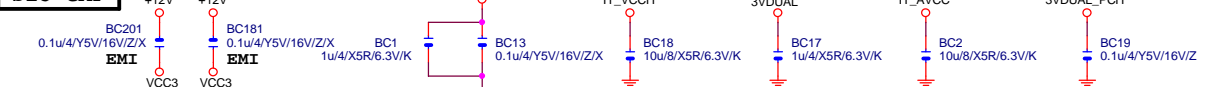
## SIO\_18V



## MB ID



## SIO CAP

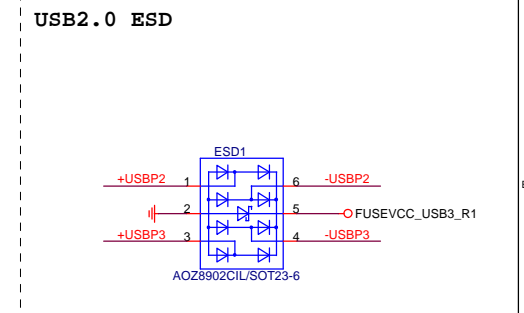
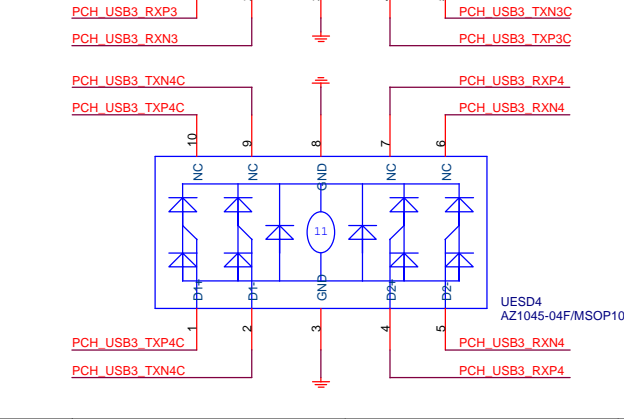
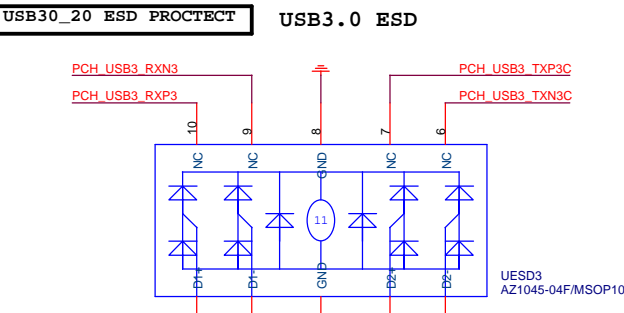
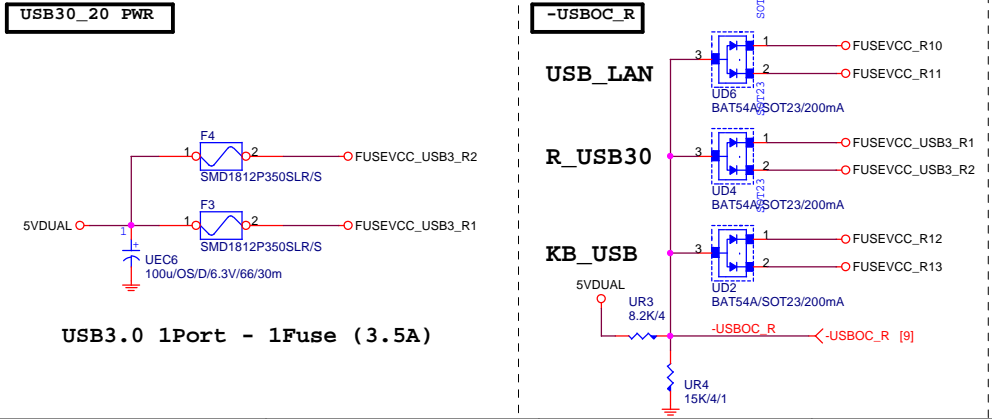
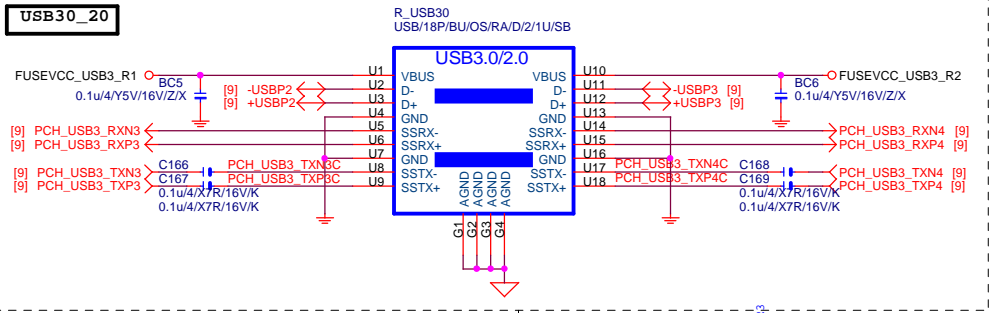
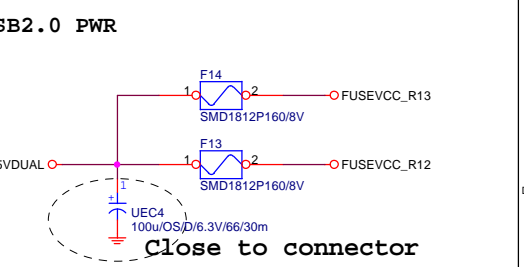
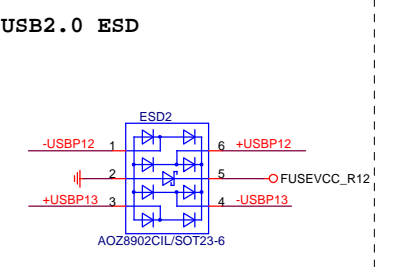
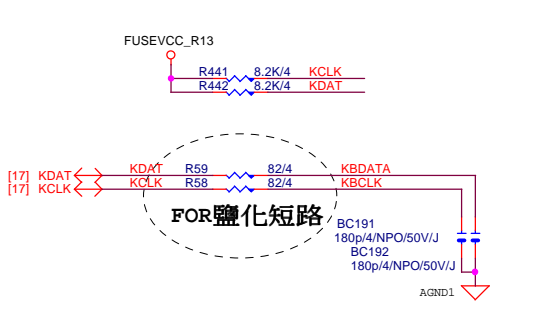
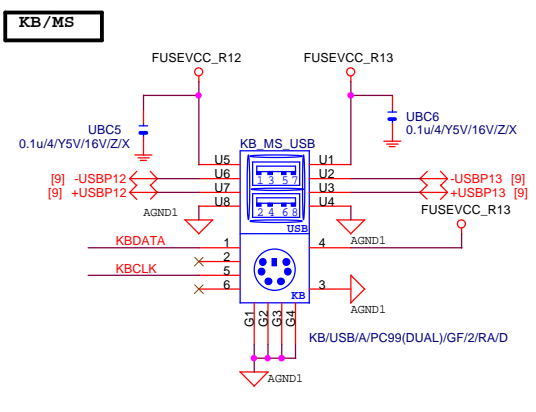
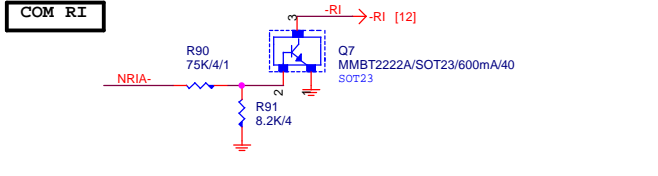
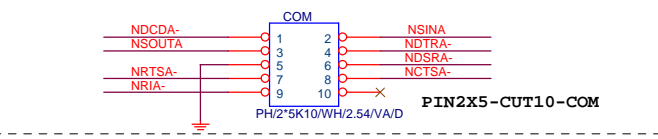
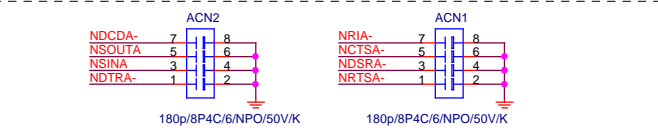
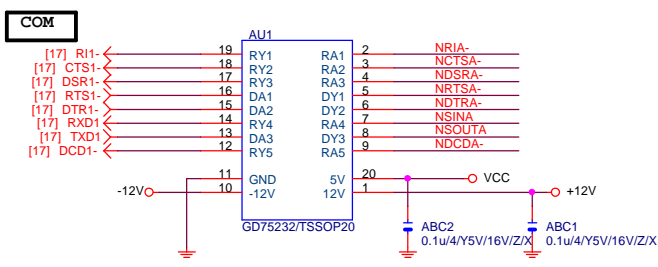


## Gigabyte Technology

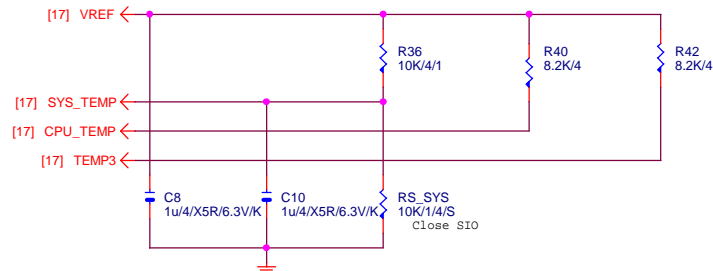
## ITE 8728 LPC IO

## GA-B75M-D3H

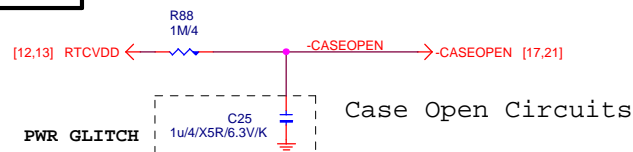
Rev 1.1



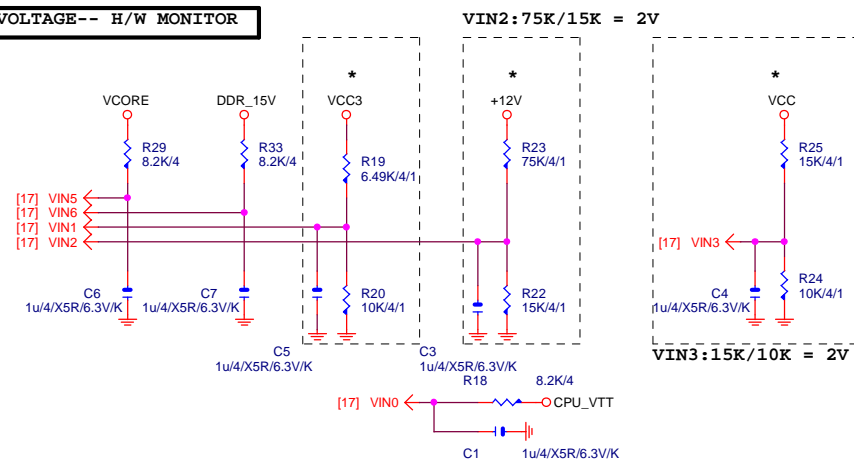
## TEMP H/W MONITOR



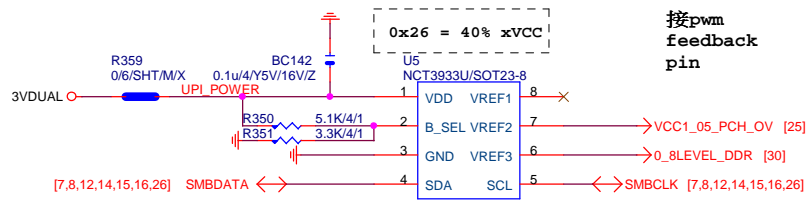
## CASE OPEN



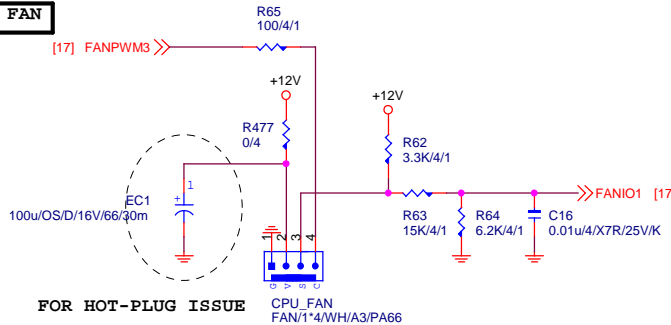
## VOLTAGE-- H/W MONITOR



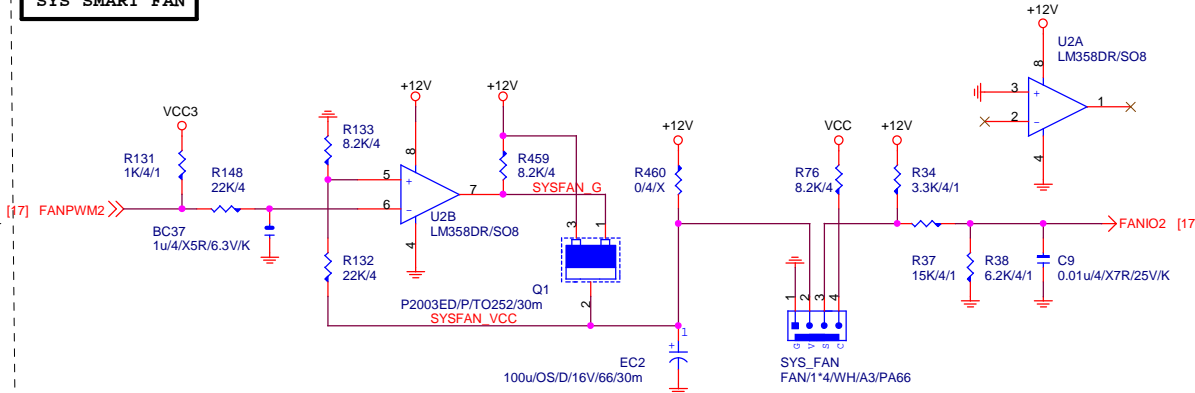
## OV NCT3933



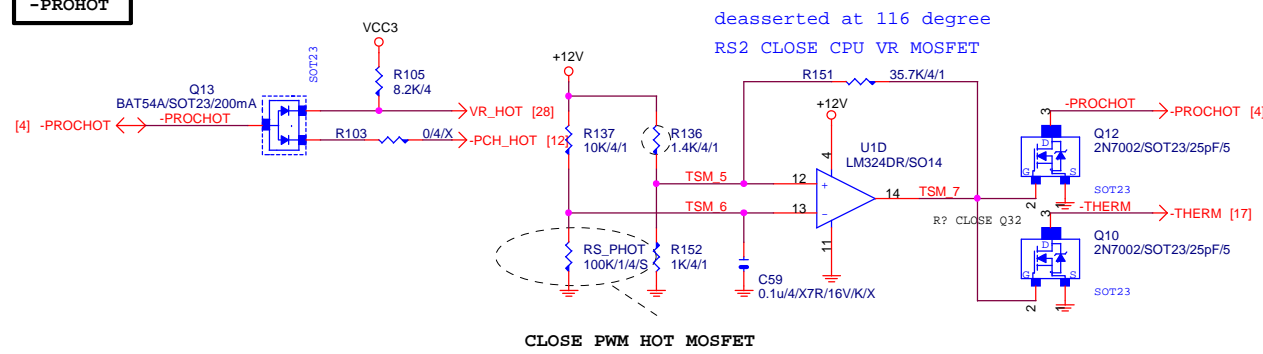
## CPU SMART FAN



## SYS SMART FAN



## -PROHOT

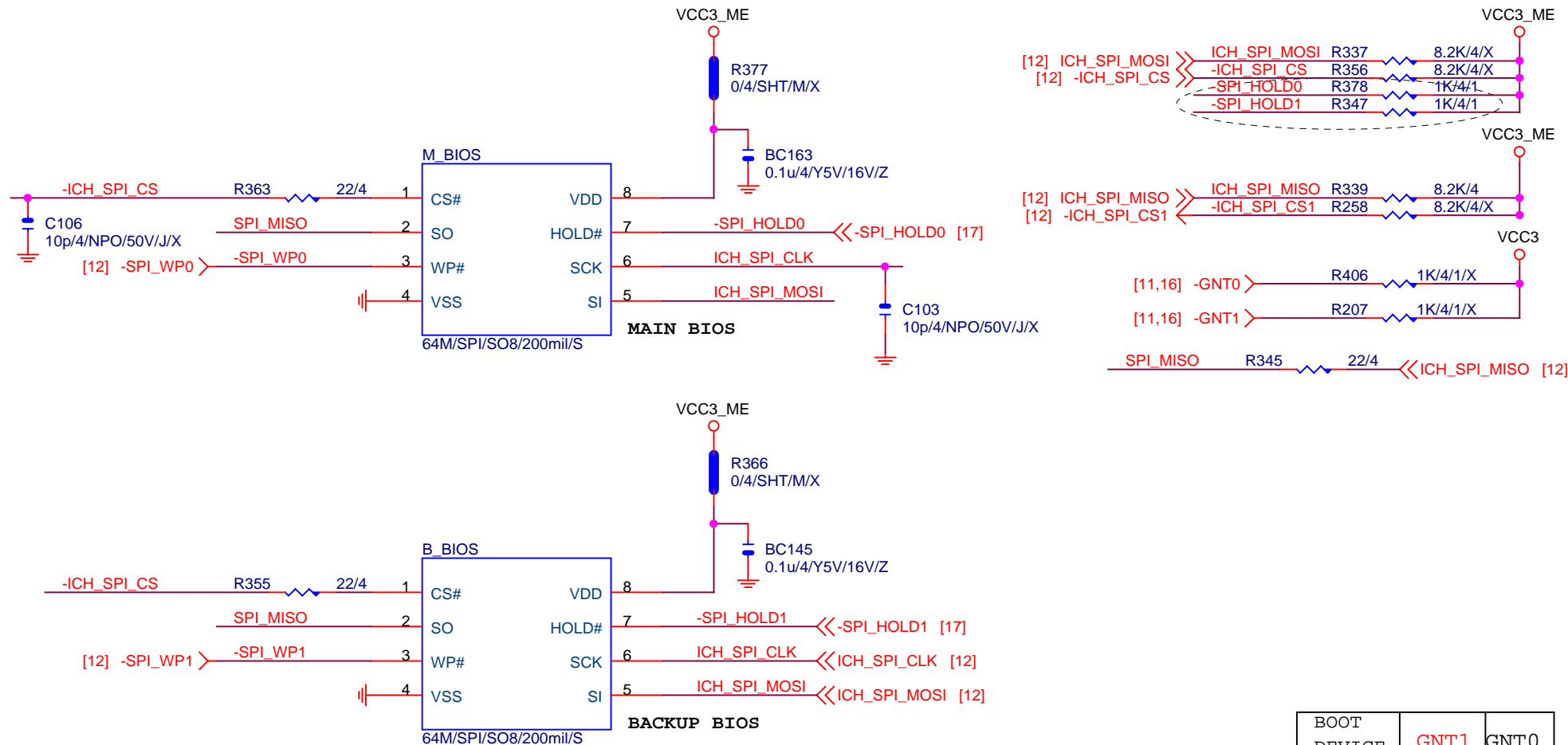


CLOSE PWM HOT MOSFET

Gigabyte Technology

Title			HWM,FAN CTRL,OV
Size			Custom
Document Number			GA-B75M-D3H
Date:			Friday, June 08, 2012
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Rev			1.1

# DUAL BIOS



B65使用64M BIOS

使用H67暫用32M

H61使用32M BIOS

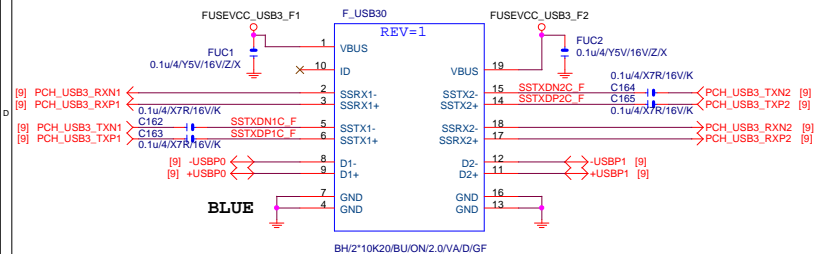
BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating  
0 means PD 1K

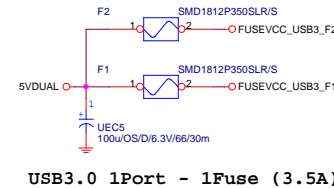
**Gigabyte Technology**

Title		
DUAL BIOS		
Size A	Document Number	Rev
	GA-B75M-D3H	1.1
Date:	Friday, June 08, 2012	Sheet 20 of 33

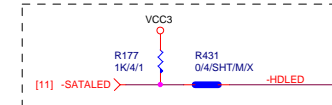
## F\_USB30



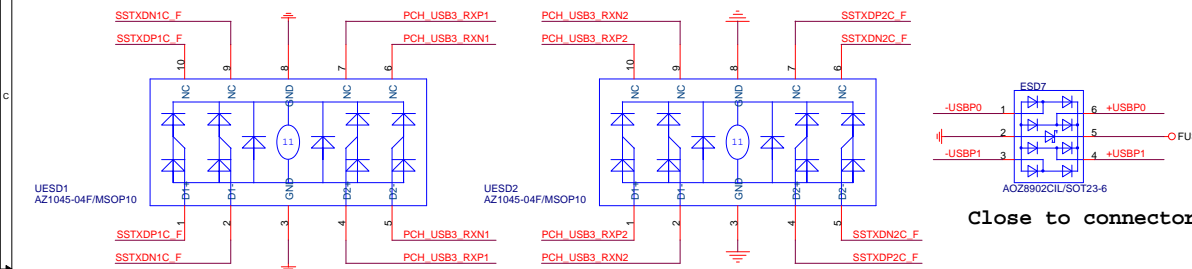
## F\_USB30 PWR



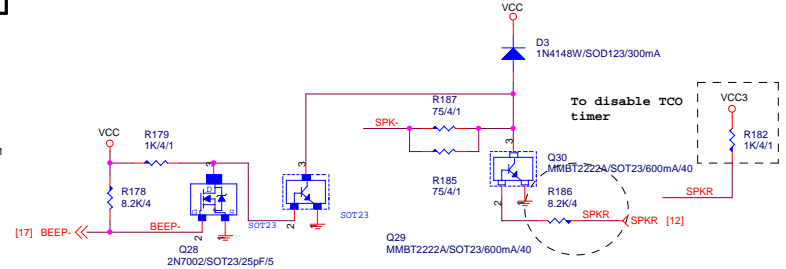
## SATA LED



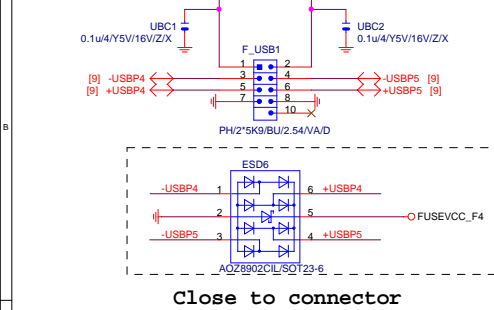
## F\_USB30 ESD PROTECT



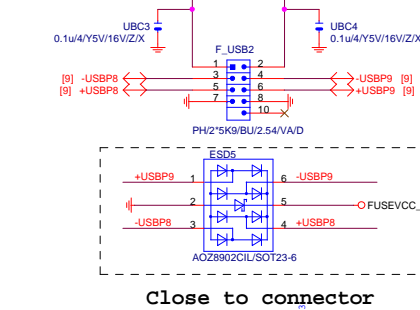
## SPKR



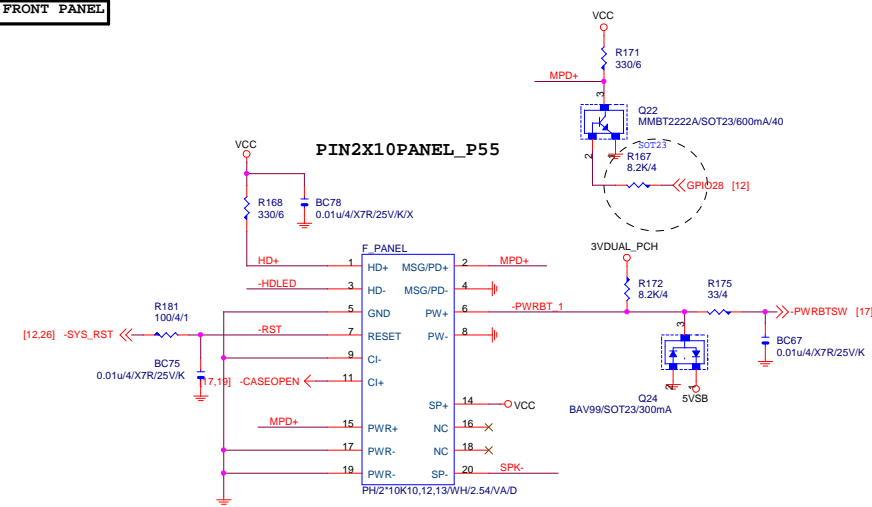
## FRONT USB1



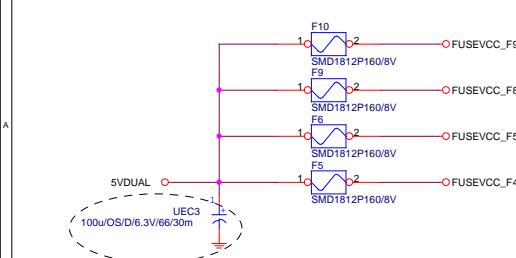
## FRONT USB2



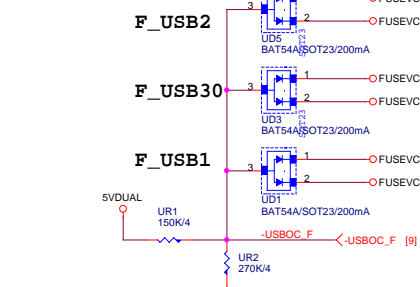
## INTEL FRONT PANEL



## FUSEVCC\_F

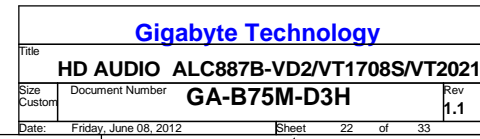


## ~USBOC\_F

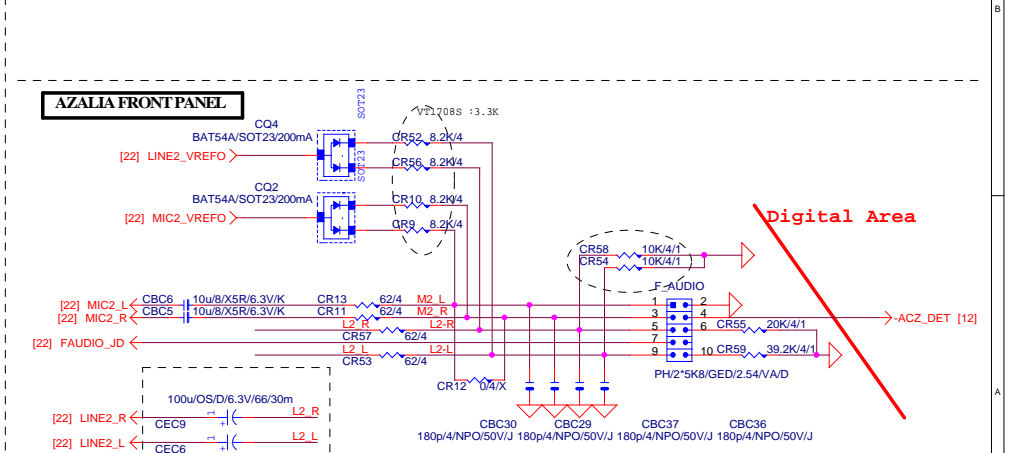
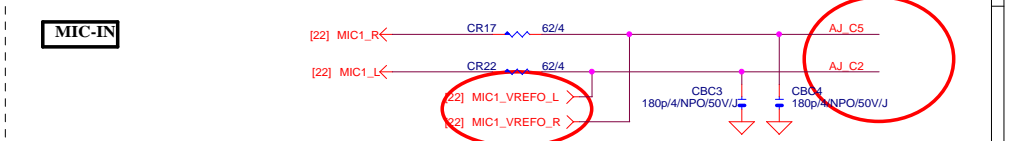
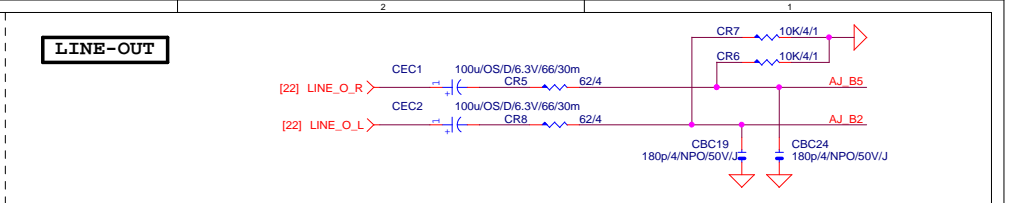


CR34: 20K/4/1% @Realtek cdec  
CR34: 5.1K/4/1% @VIA cdec  
CBC39 100P @VIA codec

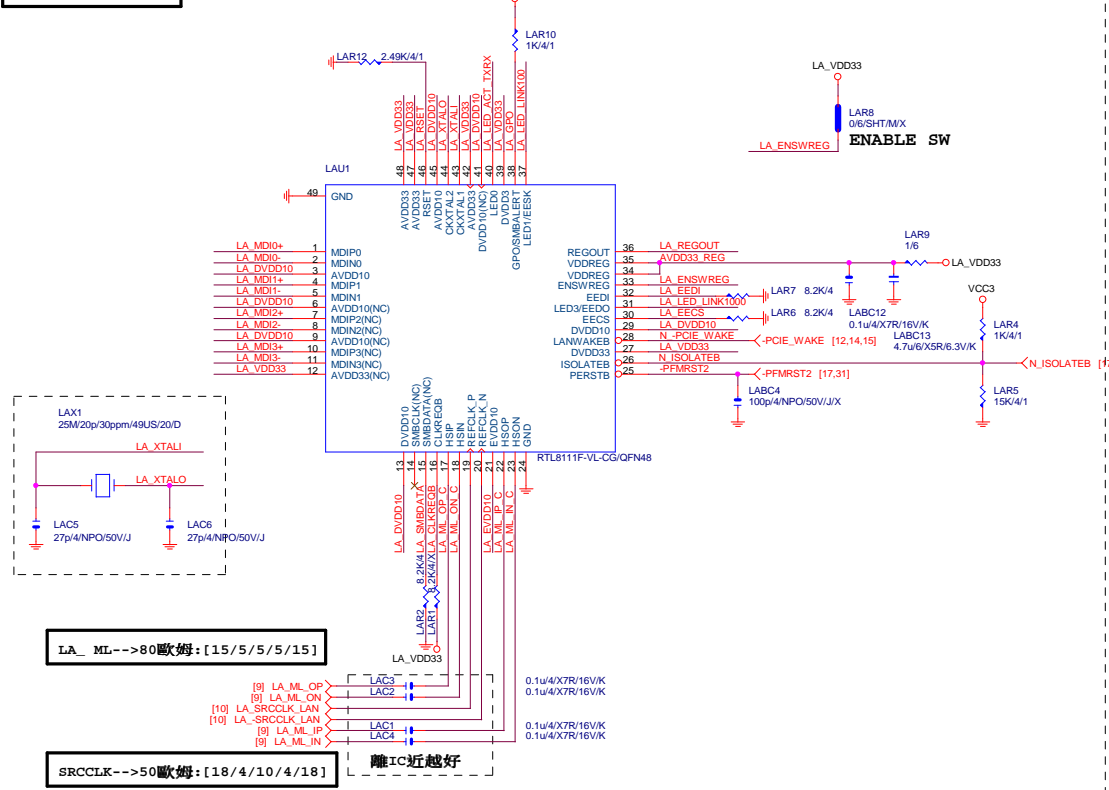
CR34 20K/4/1%  
CBC42 100pF/4/NPO/50V/J/X



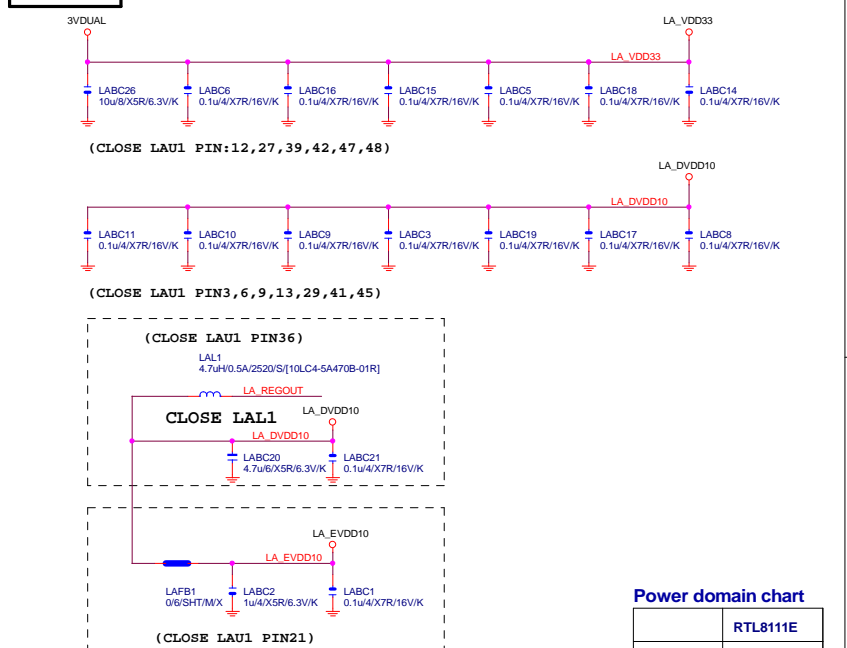




LAN:RTL8111E/VB/VL



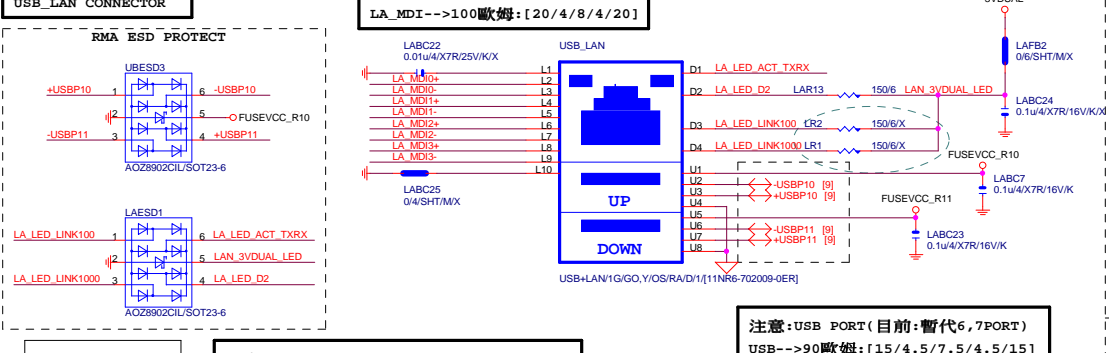
LAN POWER



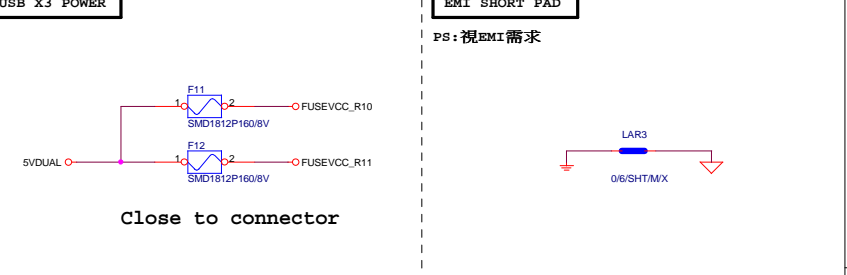
Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

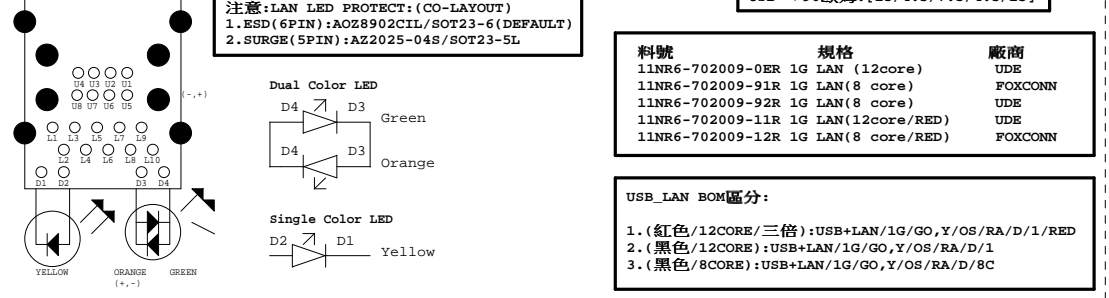
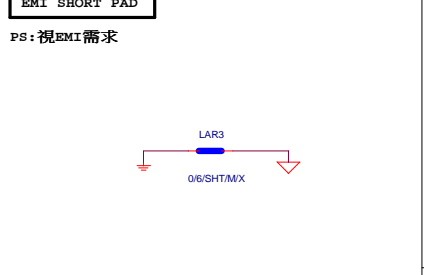
USB\_LAN CONNECTOR



USB X3 POWER



EMI SHORT PAD



注意:USB PORT(目前:暫代6,7PORT)  
USB-->90欧姆: [15/4.5/7.5/4.5/15]

料號	規格	廠商
11NR6-702009-0ER	1G LAN (12core)	UDE
11NR6-702009-91R	1G LAN (8 core)	FOXCONN
11NR6-702009-92R	1G LAN (8 core)	UDE
11NR6-702009-11R	1G LAN (12core/RED)	UDE
11NR6-702009-12R	1G LAN (8 core/RED)	FOXCONN

USB\_LAN BOM區分:

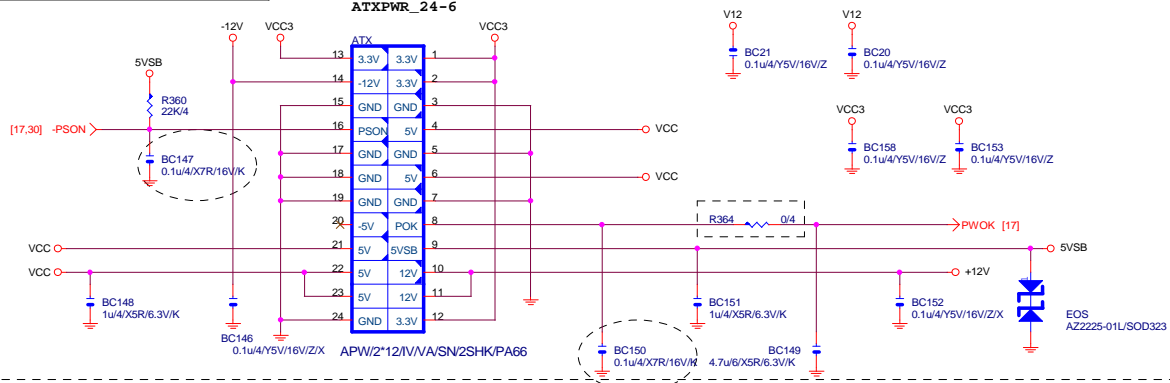
- (紅色/12CORE/三倍): USB+LAN/1G/GO, Y/OS/RA/D/1/RED
- (黑色/12CORE): USB+LAN/1G/GO, Y/OS/RA/D/1
- (黑色/8CORE): USB+LAN/1G/GO, Y/OS/RA/D/8C

Gigabyte Technology

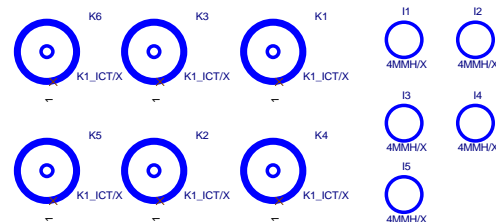
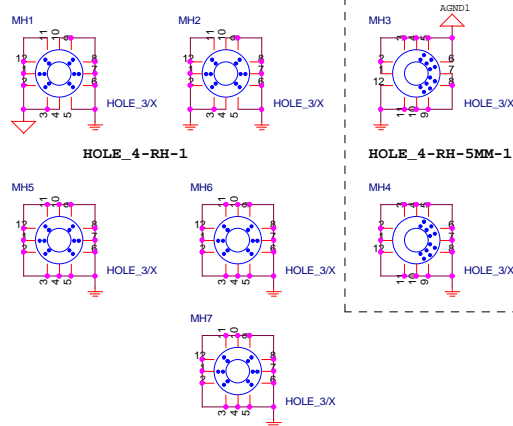
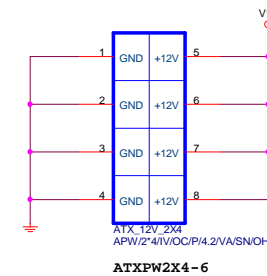
Title		Rev	
ARTHEROS AR8151/AR8161		1.1	
Size	Document Number		
Custom	GA-B75M-D3H		
Date	Friday, June 08, 2012	Sheet	24 of 33



ATXX24 POWER CONNECTOR

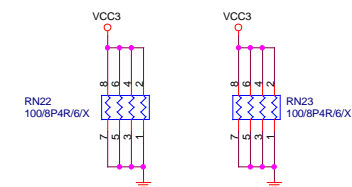


ATXX4 POWER CONNECTOR

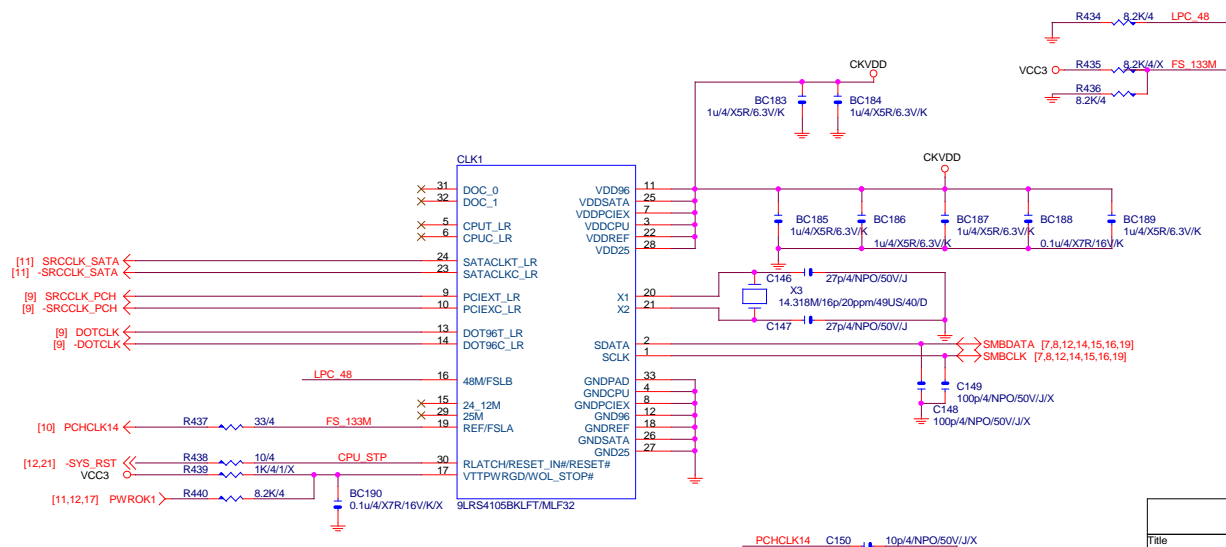


To prevent the 5VSB  
under loading when  
boot

FIX PWR MINMUN LOAD

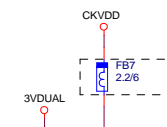


CLK GEN
---------

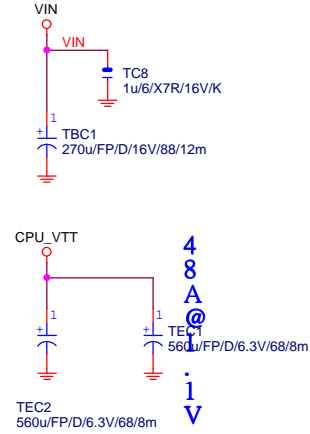
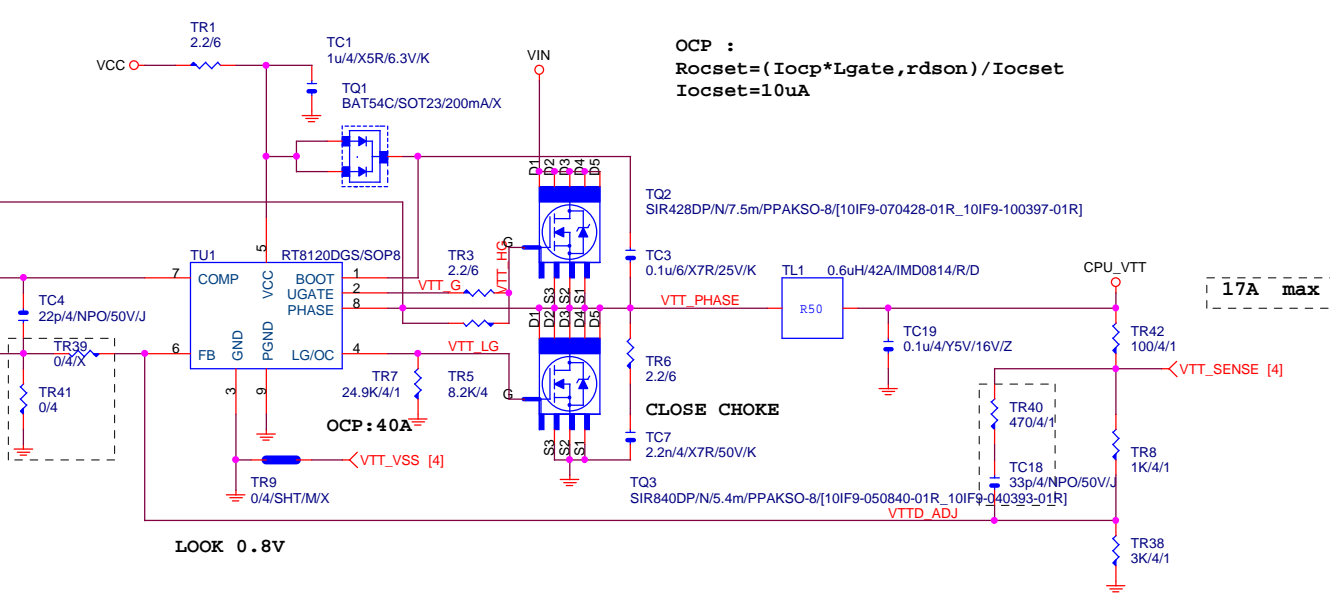


### CPU Frequency Selection

FS	CPU
0	100M <Default>
1	133M



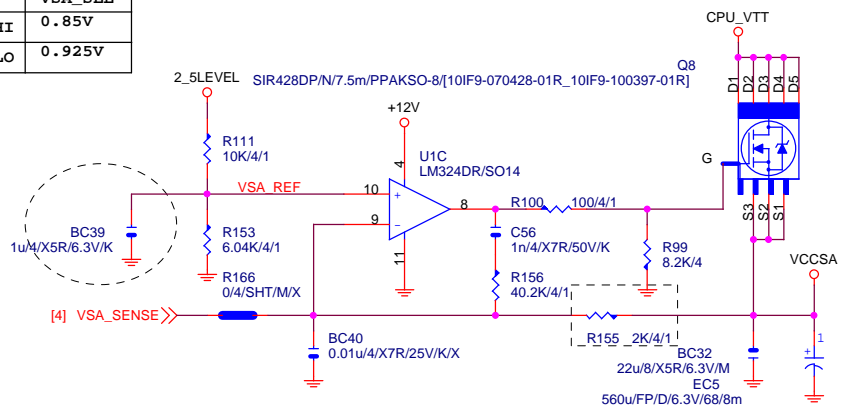
# CPU\_VTT



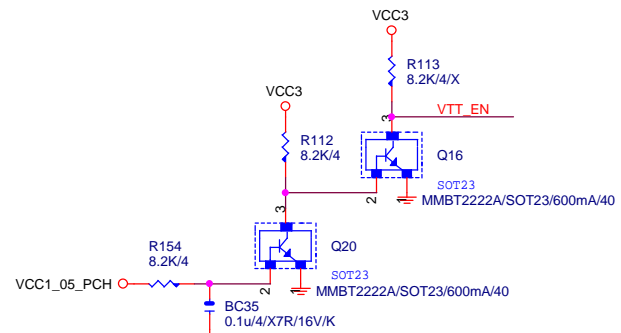
# VCCSA

PDG 0.8

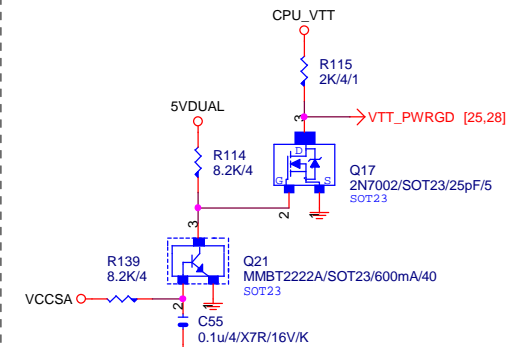
	VSA_SEL
HI	0.85V
LO	0.925V



# CPU\_VTT PWR SEQ

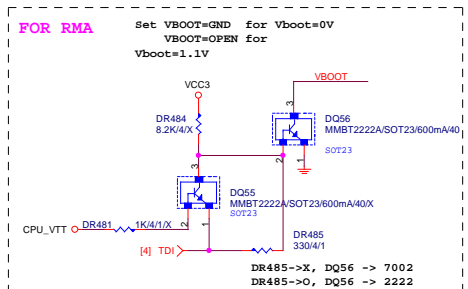
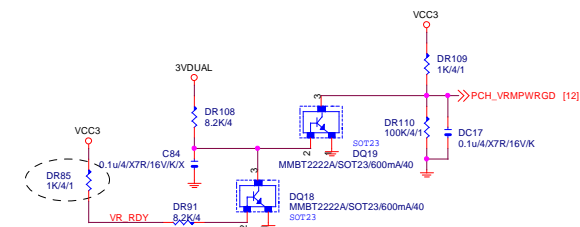
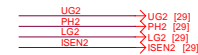
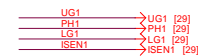
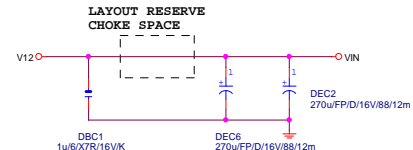


# VTT\_PWRGD



GIGABYTE

Title		
CPU_VTT PWM_ISL95870CRZ		
Size	Document Number	Rev
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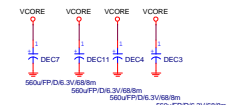
PHASE 1



**PHASE 3**



**PHASE 2**

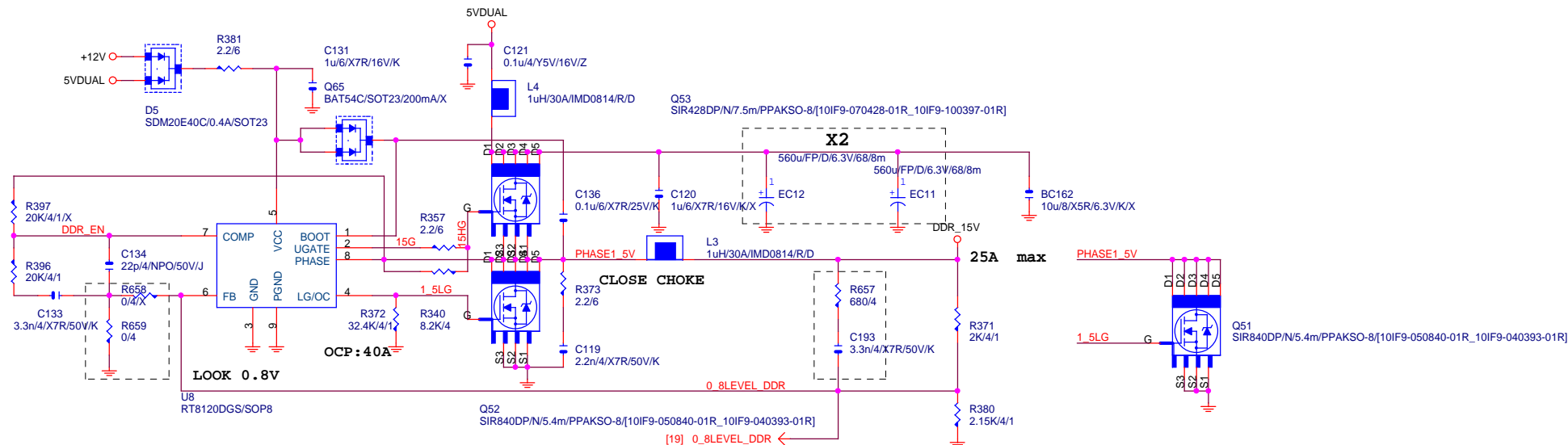


VAXG

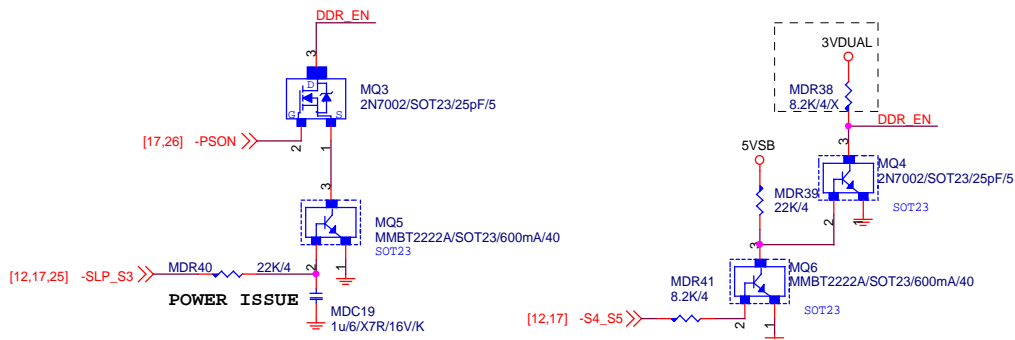




## DDR15V



PWR SEQ



VIN=5V,VOUT=1.5V,IOUT=25A,PHASE=1  
IRMS=11.45A

560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A  
Coefficient=1.7(85°C),1(105°C)

VIN Ripple current=4.7X1.7=7.99A(85°C)  
-->故固態電容須2X7.99=15.98>11.45A

OCP :  $I_{peak} = (2 \times I_{ocset} \times R_{ocset}) / R_{dson}$   
 typ  $I_{ocset} = 20\mu A$  ,  $R_{ocset} = 4.7k$   
 OCP :  $53.71A = (2 \times 20\mu A \times 4.7k) / (7m / 7m)$

## Gigabyte Technology

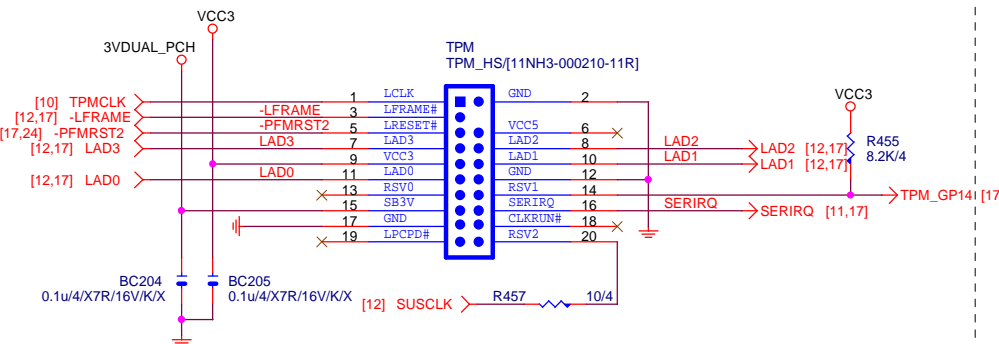
## DDR POWER

GA-B75M-D3H

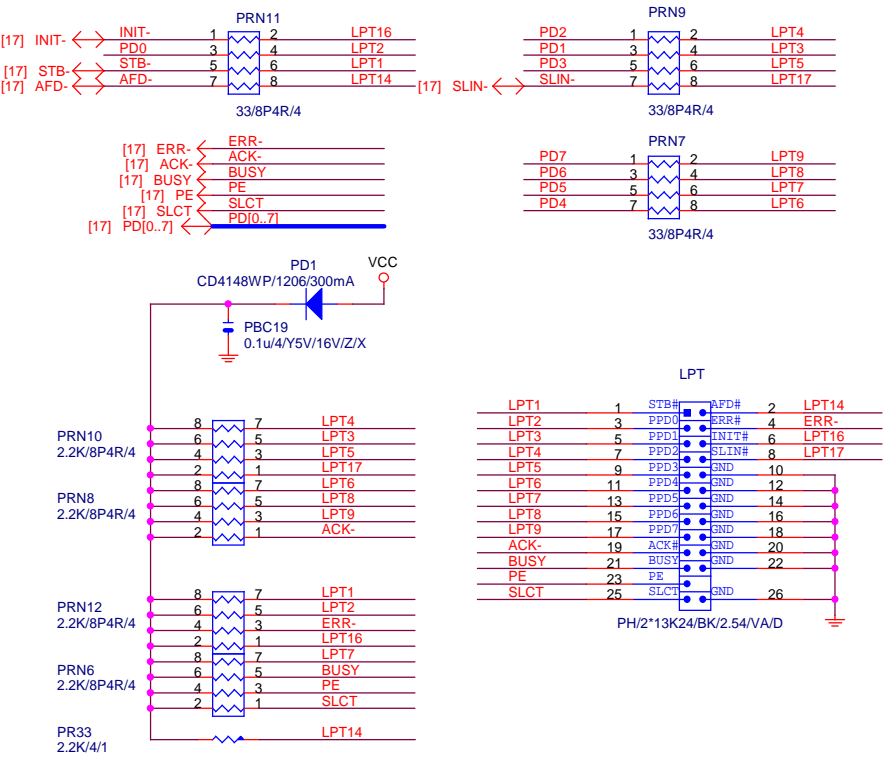
Rev  
1.1

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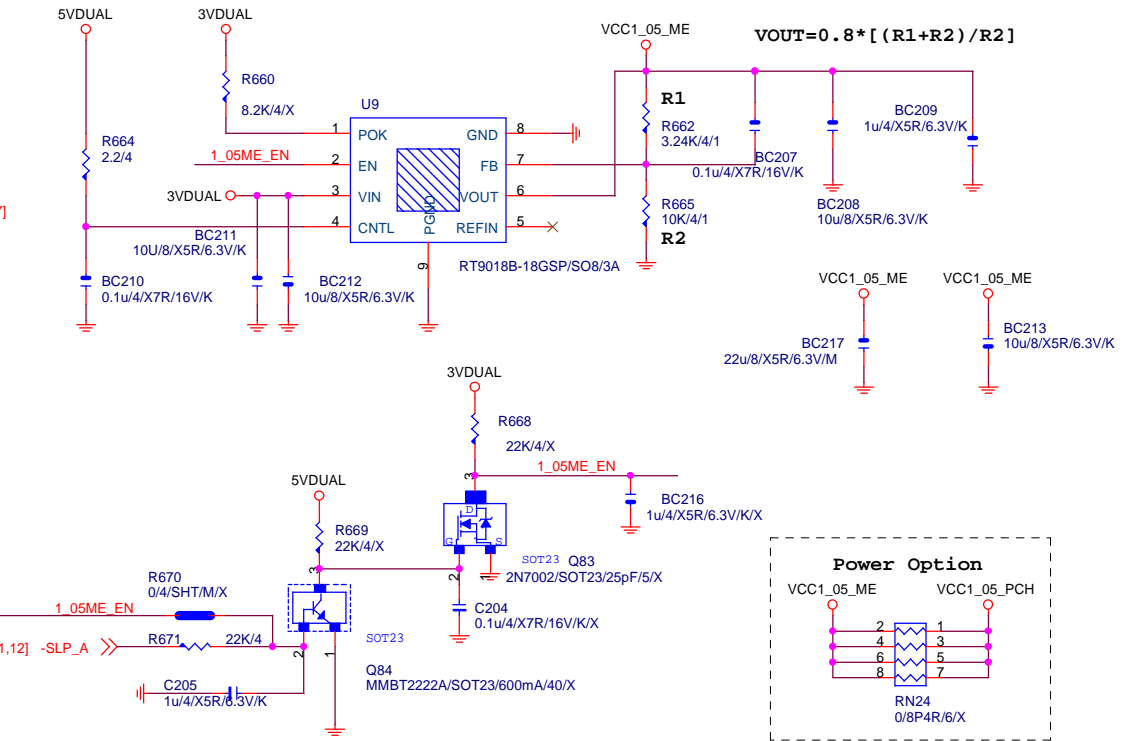
TPM



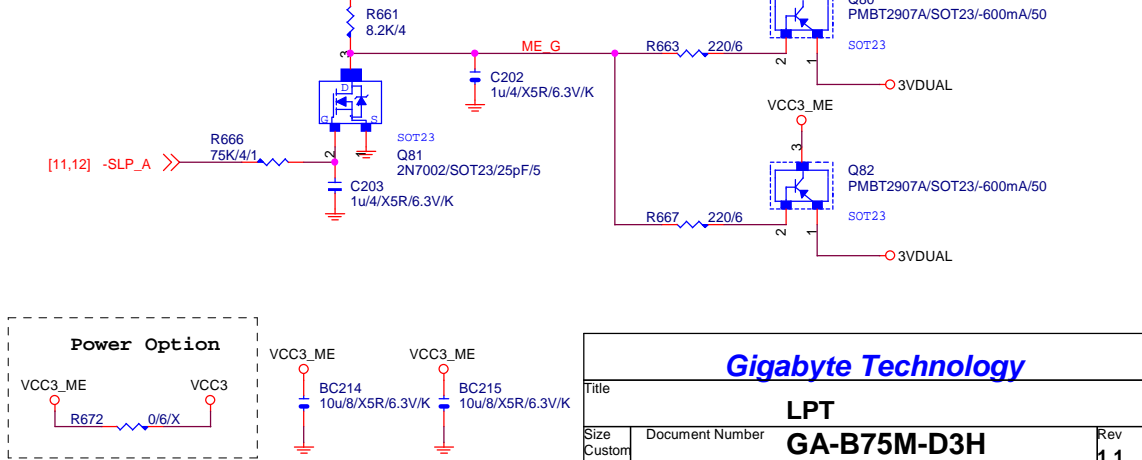
LPT PORT



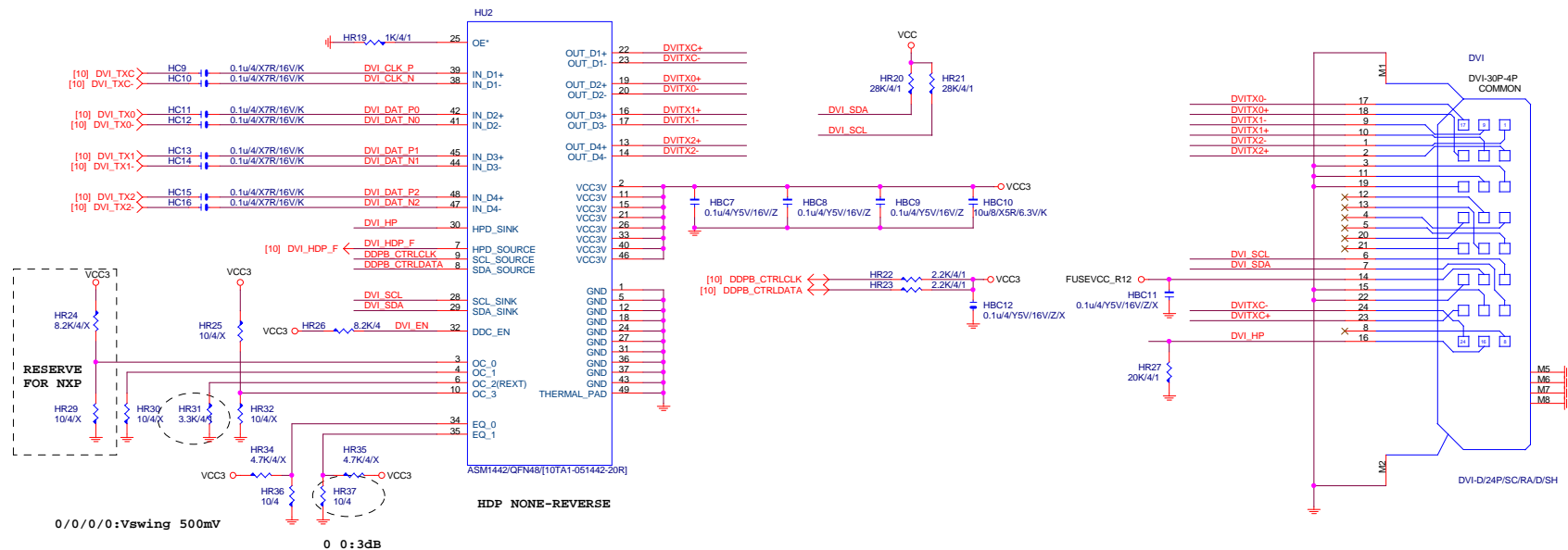
VCC1\_05\_ME



VCC3\_ME



# DVI LEVEL SHIFT



# HDMI LEVEL SHIFT

